

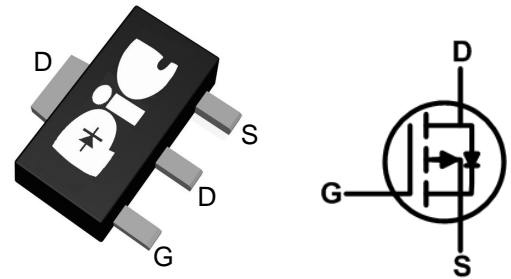
## ➤ General Description

This PAP31TK13K P-Channel enhancement mode power field effect transistor is the high density trench technology and this advanced technology can provide excellent  $R_{ds(On)}$  performance and efficiency for power switching and load switching application., this device also comply with the RoHS and Green Product requirement with full function reliability approved.

## ➤ Feature

- Super high density cell design for extremely
- low  $R_{DS(ON)}$
- SOT-89-3L package design

## ➤ SOT-89-3L



## ➤ Application

- Power Management in White LED System
- Push Pull Converter
- LCD TV Inverter & AD/DC Inverter Systems.

## ➤ Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current, $V_{GS} @ -10V^1$	$I_D@T_A=25^\circ C$	-5	A
Continuous Drain Current, $V_{GS} @ -10V^1$	$I_D@T_A=70^\circ C$	-4	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	-25	A
Total Power Dissipation <sup>3</sup>	$P_D@T_A=25^\circ C$	1.5	W
Storage Temperature Range	$T_{STG}$	-55 to 150	$^\circ C$
Operating Junction Temperature Range	$T_J$	-55 to 150	$^\circ C$
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	85	$^\circ C/W$
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	24	$^\circ C/W$

### ➤ Electrical Characteristics ( $T_J=25^\circ C$ Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V$ , $I_D=-250\mu A$	-30	---	---	V
$BV_{DSS}$ Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_J$	Reference to $25^\circ C$ , $I_D=-1mA$	---	-0.016	---	$V/^\circ C$
Static Drain-Source On-Resistance <sup>2</sup>	$R_{DS(ON)}$	$V_{GS}=-10V$ , $I_D=-5A$	---	32	40	$m\Omega$
		$V_{GS}=-4.5V$ , $I_D=-4A$	---	50	62	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}$ , $I_D=-250\mu A$	-1.2	-1.5	-2.5	V
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}$		---	-2.8	---	$mV/^\circ C$
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=-24V$ , $V_{GS}=0V$ , $T_J=25^\circ C$	---	---	-1	$\mu A$
		$V_{DS}=-24V$ , $V_{GS}=0V$ , $T_J=55^\circ C$	---	---	-5	
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V$ , $V_{DS}=0V$	---	---	$\pm 100$	nA
Forward Transconductance	$g_{fs}$	$V_{DS}=-5V$ , $I_D=-5A$	---	13.7	---	S
Gate Resistance	$R_g$	$V_{DS}=0V$ , $V_{GS}=0V$ , $f=1MHz$	---	15	30	$\Omega$
Total Gate Charge (-4.5V)	$Q_g$	$V_{DS}=-15V$ , $V_{GS}=-4.5V$ , $I_D=-5A$	---	9.8	14	nC
Gate-Source Charge	$Q_{gs}$		---	3.6	5.1	
Gate-Drain Charge	$Q_{gd}$		---	3.2	4.5	
Turn-On Delay Time	$T_{d(on)}$	$V_{DD}=-15V$ , $V_{GS}=-10V$ , $R_G=3.3\Omega$ , $I_D=-5A$	---	5.0	10	ns
Rise Time	$T_r$		---	27.2	49	
Turn-Off Delay Time	$T_{d(off)}$		---	41	82	
Fall Time	$T_f$		---	17.5	35	
Input Capacitance	$C_{iss}$	$V_{DS}=-15V$ , $V_{GS}=0V$ , $f=1MHz$	---	1100	1540	pF
Output Capacitance	$C_{oss}$		---	150	210	
Reverse Transfer Capacitance	$C_{rss}$		---	125	175	

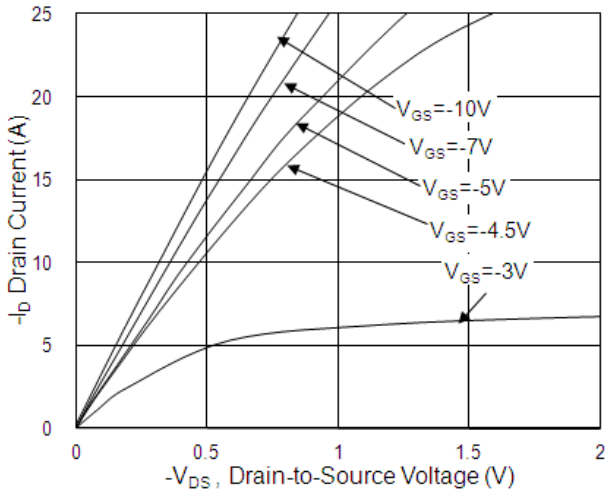
### ➤ Diode Characteristics

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Continuous Source Current <sup>1,4</sup>	$I_S$	$V_G=V_D=0V$ , Force Current	---	---	-5	A
Pulsed Source Current <sup>2,4</sup>	$I_{SM}$		---	---	-25	A
Diode Forward Voltage <sup>2</sup>	$V_{SD}$	$V_{GS}=0V$ , $I_S=-1A$ , $T_J=25^\circ C$	---	---	-1.2	V
Reverse Recovery Time	$t_{rr}$	$I_F=5A$ , $dI/dt=100A/\mu s$ , $T_J=25^\circ C$	---	6.2	---	nS
Reverse Recovery Charge	$Q_{rr}$		---	1.0	---	nC

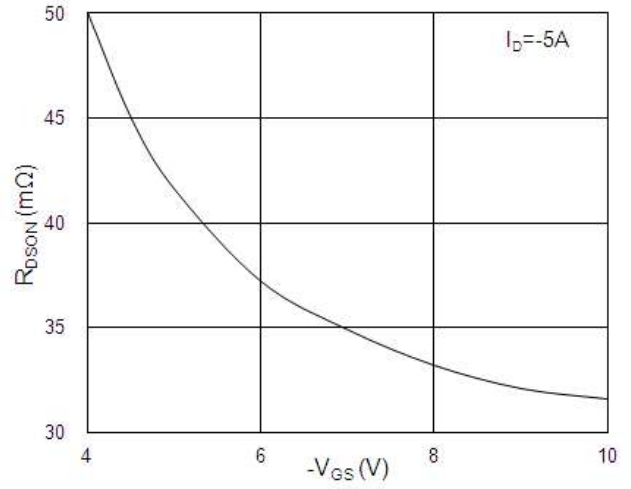
Note :

1. Pulse width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$
3. Ensure that the channel temperature does not exceed  $150^\circ C$ .
4. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.

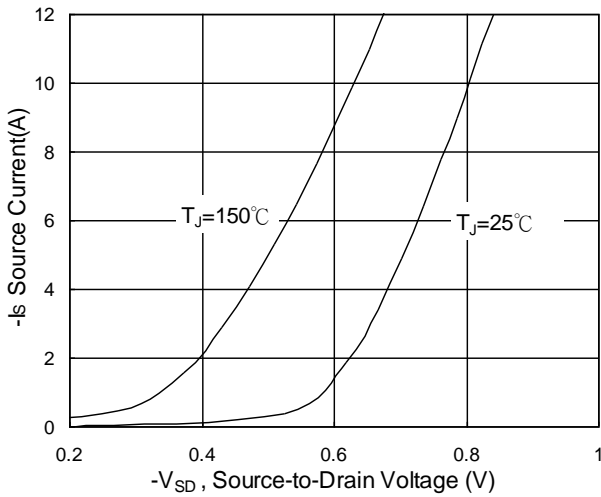
## ➤ Typical Characteristics



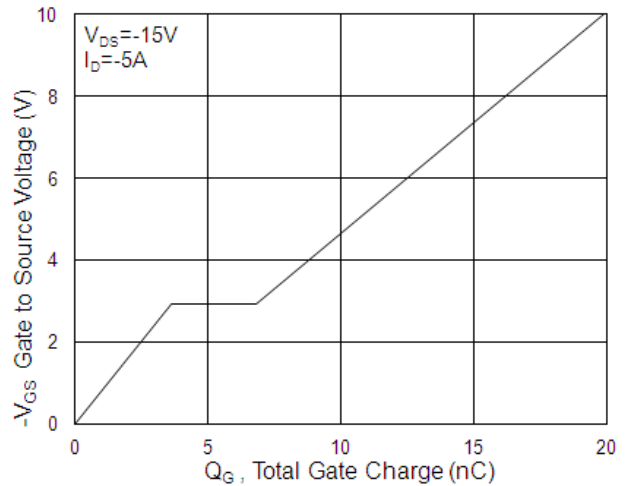
**Fig.1 Typical Output Characteristics**



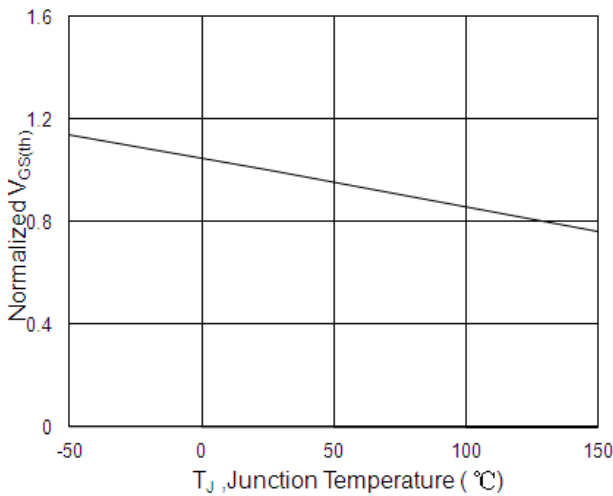
**Fig.2 On-Resistance v.s Gate-Source**



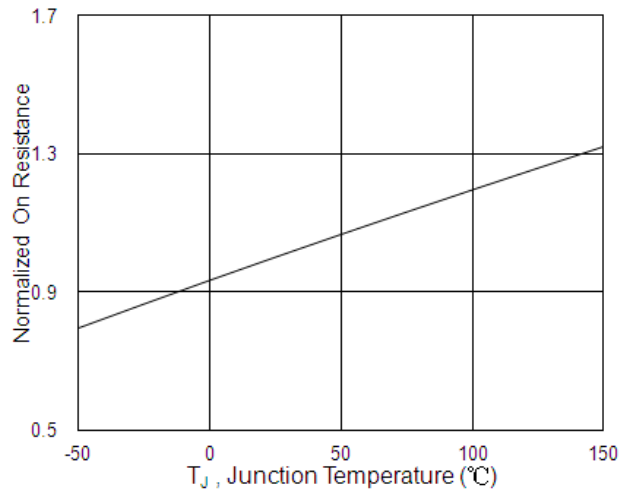
**Fig.3 Forward Characteristics of Reverse**



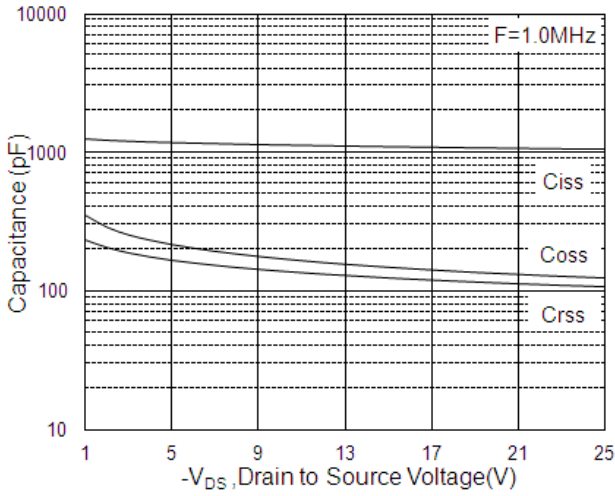
**Fig.4 Gate-Charge Characteristics**



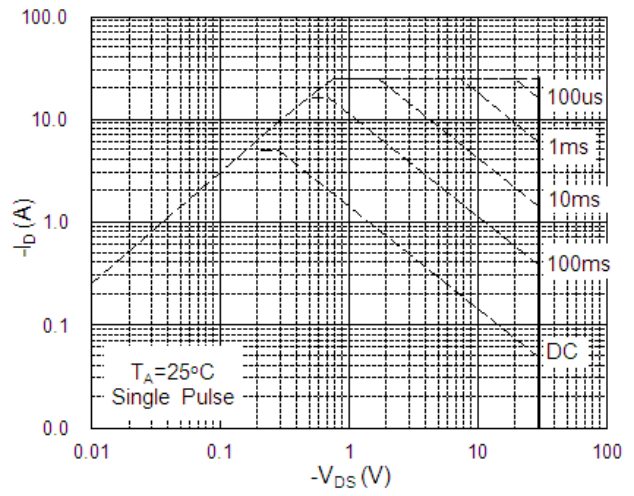
**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**



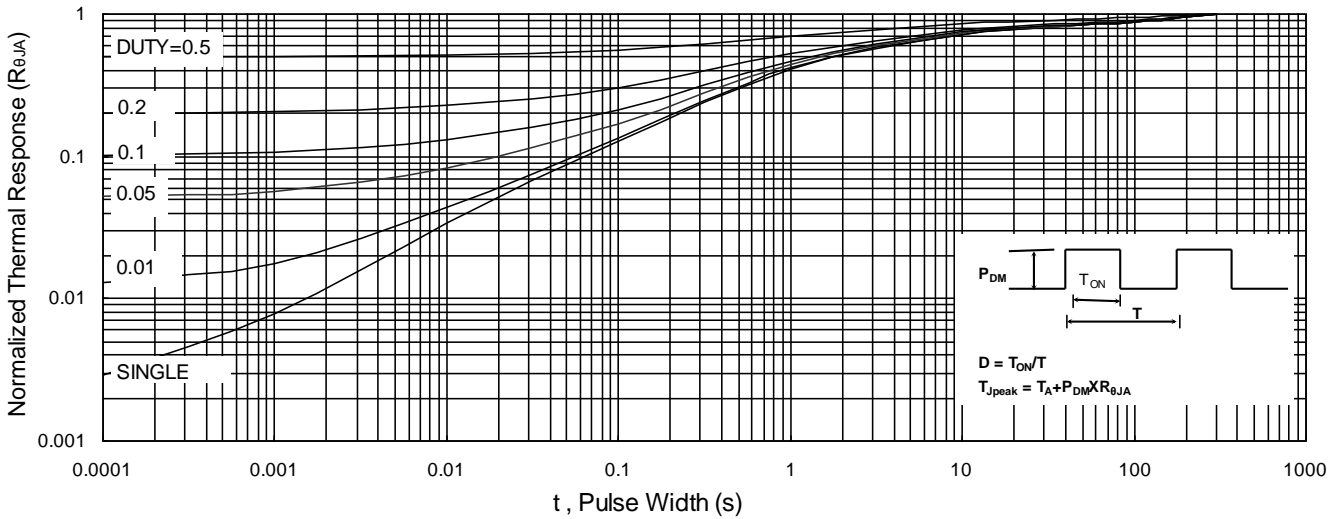
**Fig.6 Normalized  $R_{DS(ON)}$  vs.  $T_J$**



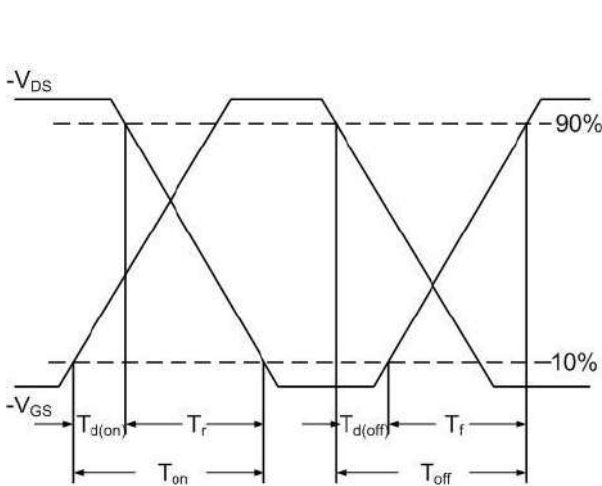
**Fig.7 Capacitance**



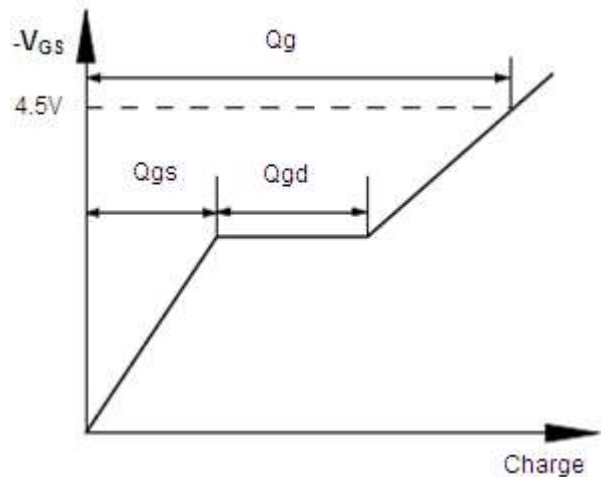
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



**Fig.10 Switching Time Waveform**



**Fig.11 Gate Charge Waveform**

## ➤ Recommand IR Reflow Soldering Thermal Profile

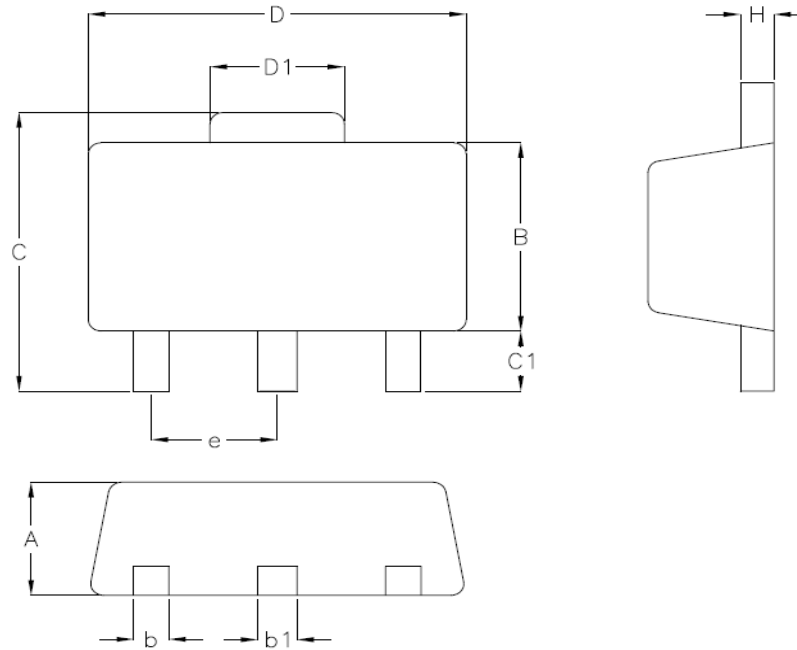


Profile Feature	Pb-Free Assembly Profile
Temperature Min. (T <sub>smin</sub> )	150°C
Temperature Max. (T <sub>smax</sub> )	200°C
Time (t <sub>s</sub> ) from (T <sub>smin</sub> to T <sub>smax</sub> )	60-120 seconds
Average Ramp-up Rate (t <sub>L</sub> to t <sub>P</sub> )	3°C/second max.
Liquidous Temperature (T <sub>L</sub> )	217°C
Time (t <sub>L</sub> ) Maintained Above (T <sub>L</sub> )	60 – 150 seconds
Peak Temperature	260°C +0°C / -5°C
Time (t <sub>P</sub> ) within 5°C of actual Peak Temperature	30 seconds
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.

## ➤ Ordering Information

Part Number	Description	Quantity
PAP31TK13K	SOT-89-3L Reel	1000 pcs

➤ Package Information (SOT-89-3L)



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.397	1.600	0.055	0.063
b	0.420	0.540	0.017	0.021
b1	0.420	0.540	0.017	0.021
B	2.388	2.591	0.094	0.102
C	3.937	4.242	0.155	0.167
C1	0.787	1.194	0.031	0.047
D	4.394	4.597	0.173	0.181
D1	1.397	1.753	0.055	0.069
e	1.448	1.549	0.057	0.061
H	0.350	0.44	0.014	0.017

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