

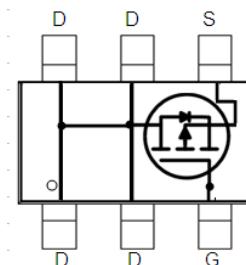
➤ General Description

This PAP31S11C P-Channel enhancement mode power field effect transistor is the high density trench technology and this advanced technology can provide excellent Rds(On) performance and efficiency for power switching and load switching application., this device also comply with the RoHS and Green Product requirement with full function reliability approved.

➤ Feature

- Super Low Gate Charge
- Green Device Available
- Excellent CdV/dt effect decline
- ESD Protected
- Advanced high cell density Trench technology
- TSOP-6 package design

➤ TSOP-6



➤ Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V _{DS}	-30	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current, V _{GS} @ -10V ₁	I _D @T _A =25°C	-5	A
Continuous Drain Current, V _{GS} @ -10V ₁	I _D @T _A =70°C	-4	A
Pulsed Drain Current ₂	I _{DM}	-20	A
Total Power Dissipation ₄	P _D @T _A =25°C	1.9	W
Operating Junction Temperature Range	T _{STG}	-55 to 150	°C
Operating Junction Temperature Range	T _J	-55 to 150	°C
Thermal Resistance Junction-Ambient ₁	R _{θJA}	65	°C/W
Thermal Resistance Junction-Ambient ₁ (t<5s)	R _{θJA}	50	°C/W

➤ **Electrical Characteristics (T_J=25°C Unless otherwise noted)**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V , I _D =-250uA	-30	---	---	V
Static Drain-Source On-Resistance ²	R _{DS(ON)}	V _{GS} =-10V , I _D =-3A	---	43	52	mΩ
		V _{GS} =-4.5V , I _D =-2A	---	66	83	
Gate Threshold Voltage	V _{GS(th)}	V _{GS} =V _{DS} , I _D =-250uA	-1.0	-1.5	-2.5	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =-24V , V _{GS} =0V , T _J =25°C	---	---	-1	uA
		V _{DS} =-24V , V _{GS} =0V , T _J =55°C	---	---	-5	
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V , V _{DS} =0V	---	---	±100	nA
Forward Transconductance	g _f	V _{DS} =-5V , I _D =-3A	---	11	---	S
Total Gate Charge (-4.5V)	Q _g	V _{DS} =-15V , V _{GS} =-4.5V , I _D =-3A	---	6.4	9.0	nC
Gate-Source Charge	Q _{gs}		---	2.3	3.2	
Gate-Drain Charge	Q _{gd}		---	1.9	2.7	
Turn-On Delay Time	T _{d(on)}	V _{DD} =-15V , V _{GS} =-10V , R _G =3.3Ω, I _D =-3A	---	2.8	5.6	ns
Rise Time	T _r		---	8.4	15.1	
Turn-Off Delay Time	T _{d(off)}		---	39	78.0	
Fall Time	T _f		---	6	12.0	
Input Capacitance	C _{iss}	V _{DS} =-15V , V _{GS} =0V , f=1MHz	---	583	816	pF
Output Capacitance	C _{oss}		---	100	140	
Reverse Transfer Capacitance	C _{rss}		---	80	112	

➤ **Diode Characteristics**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Continuous Source Current ^{1,4}	I _s	V _G =V _D =0V , Force Current	---	---	-5	A
Diode Forward Voltage ²	V _{SD}	V _{GS} =0V , I _s =-1A , T _J =25°C	---	---	-1	V

Note :

- 1.Pulse width limited by maximum junction temperature.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.Ensure that the channel temperature does not exceed 150°C.
- 4.The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

➤ Typical Characteristics

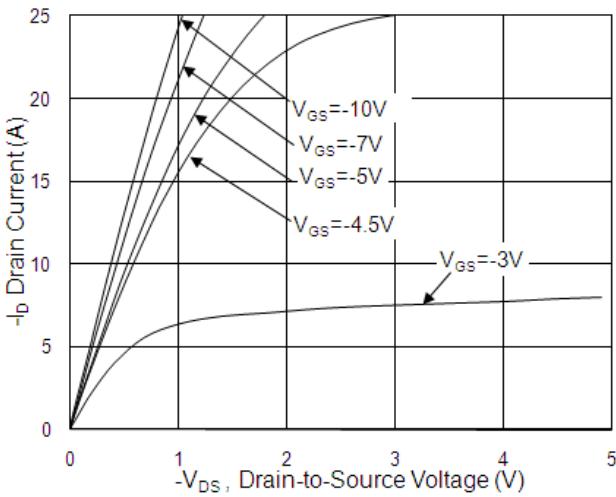


Fig.1 Typical Output Characteristics

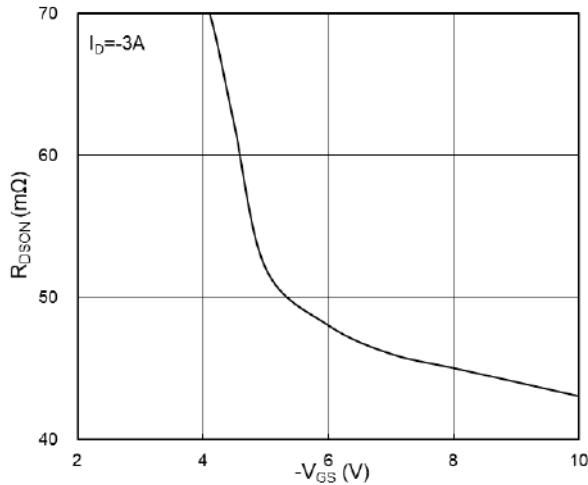


Fig.2 On-Resistance vs G-S Voltage

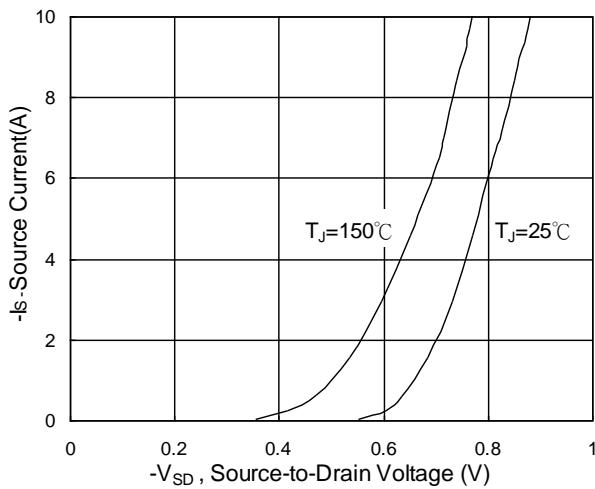


Fig.3 Source Drain Forward Characteristics

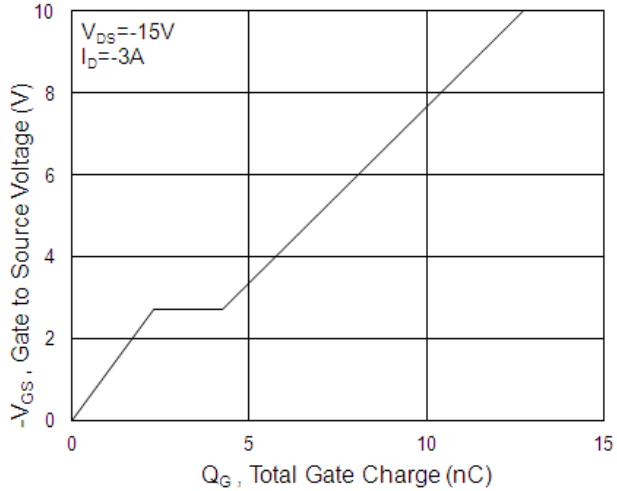


Fig.4 Gate-Charge Characteristics

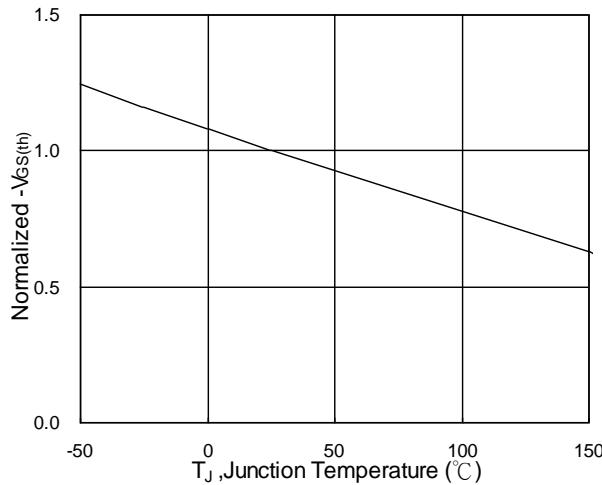


Fig.5 Normalized $V_{GS(th)}$ vs T_J

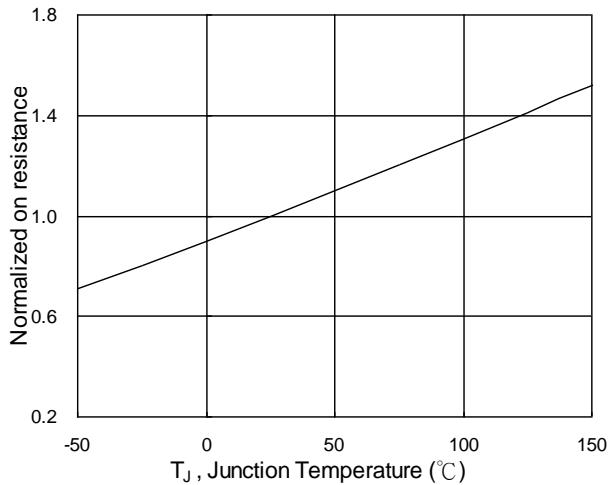
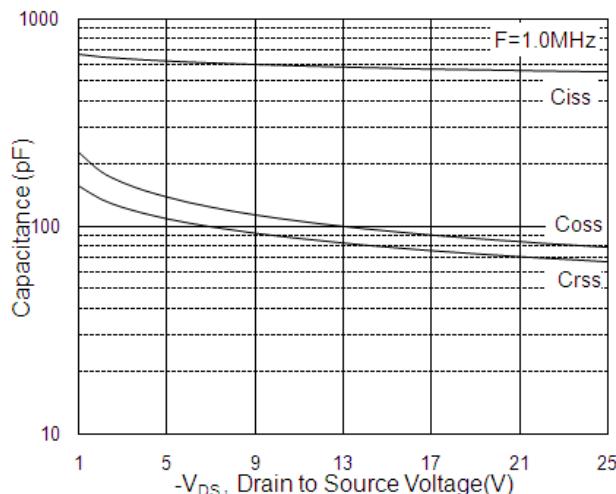
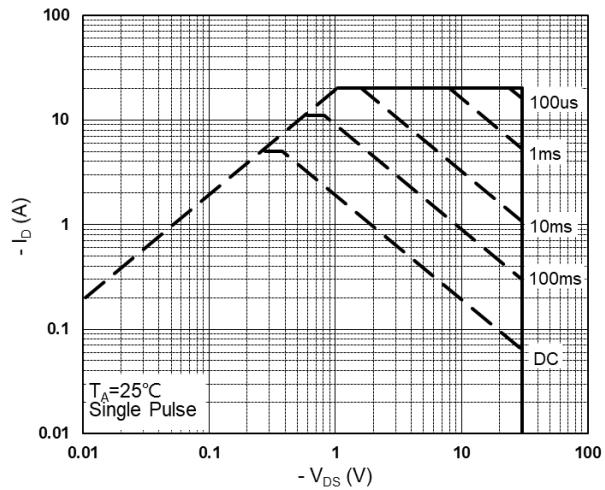
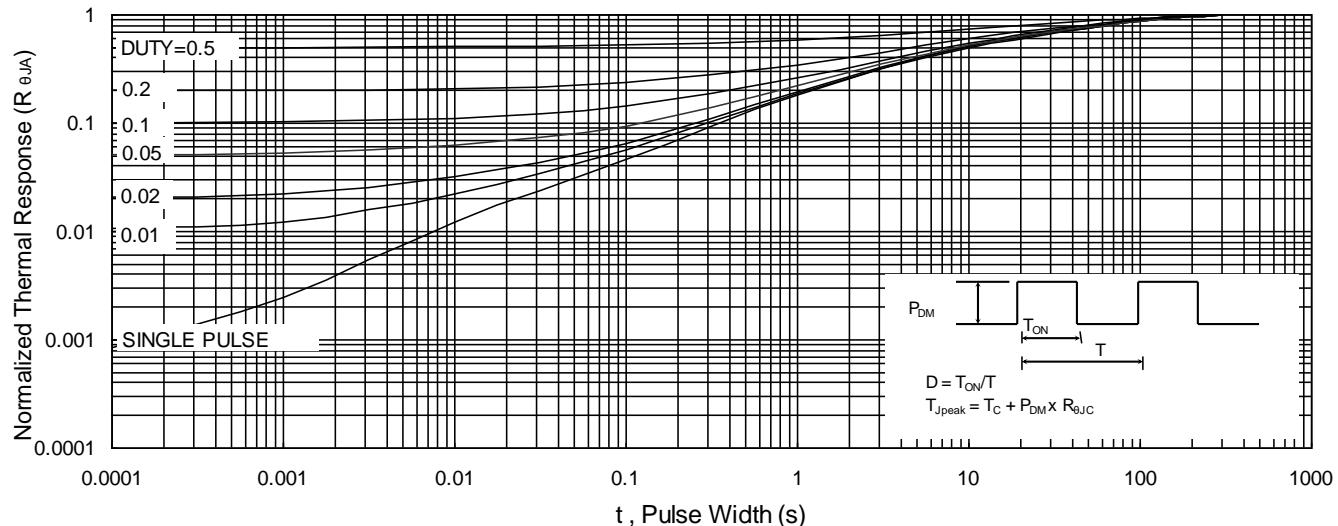
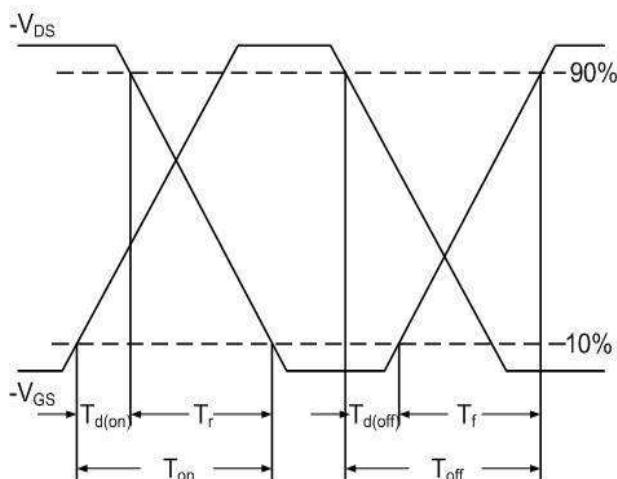
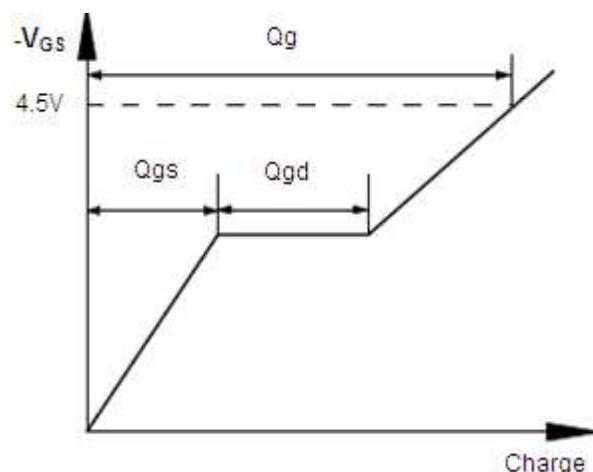
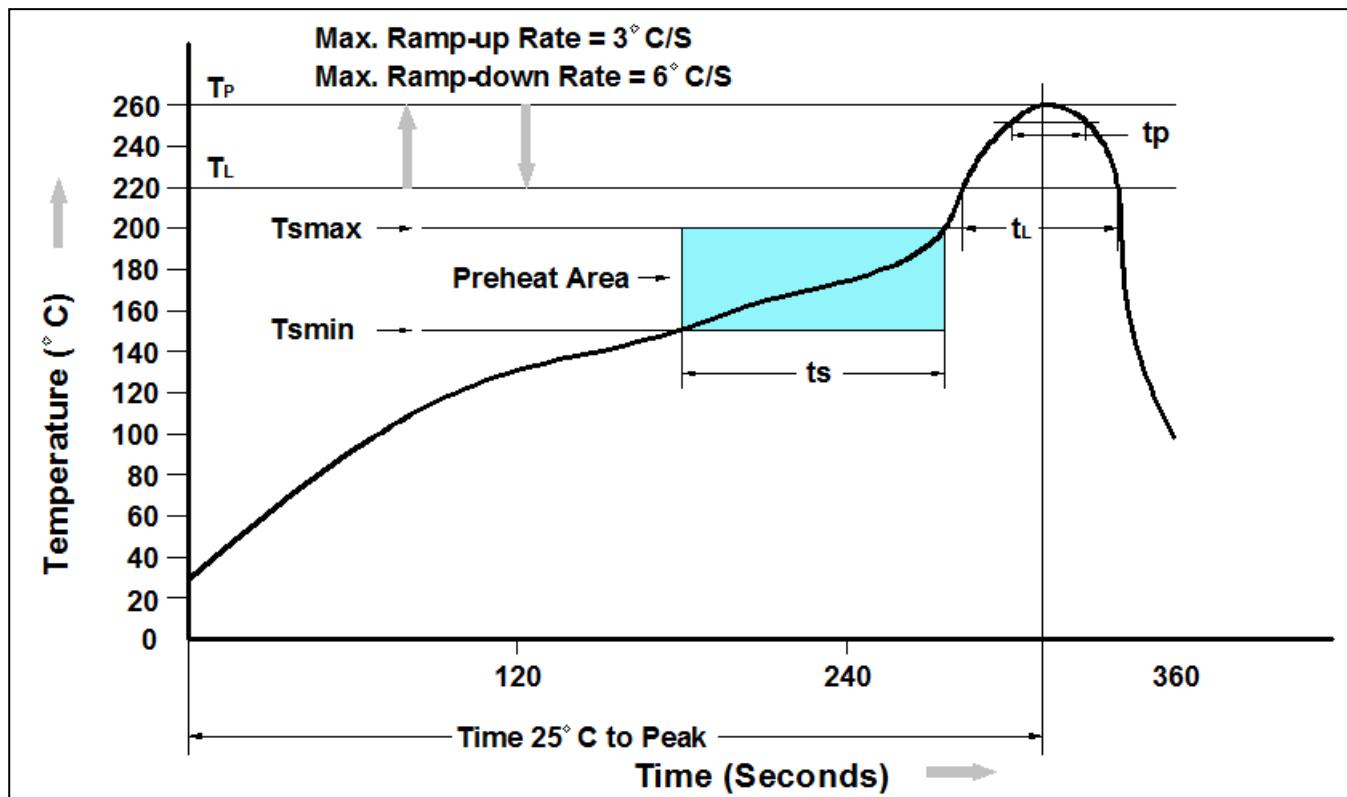


Fig.6 Normalized $R_{DS(on)}$ vs T_J


Fig.7 Capacitance

Fig.8 Safe Operating Area

Fig.9 Normalized Maximum Transient Thermal Impedance

Fig.10 Switching Time Waveform

Fig.11 Gate Charge Waveform

➤ **Recommand IR Reflow Soldering Thermal Profile**

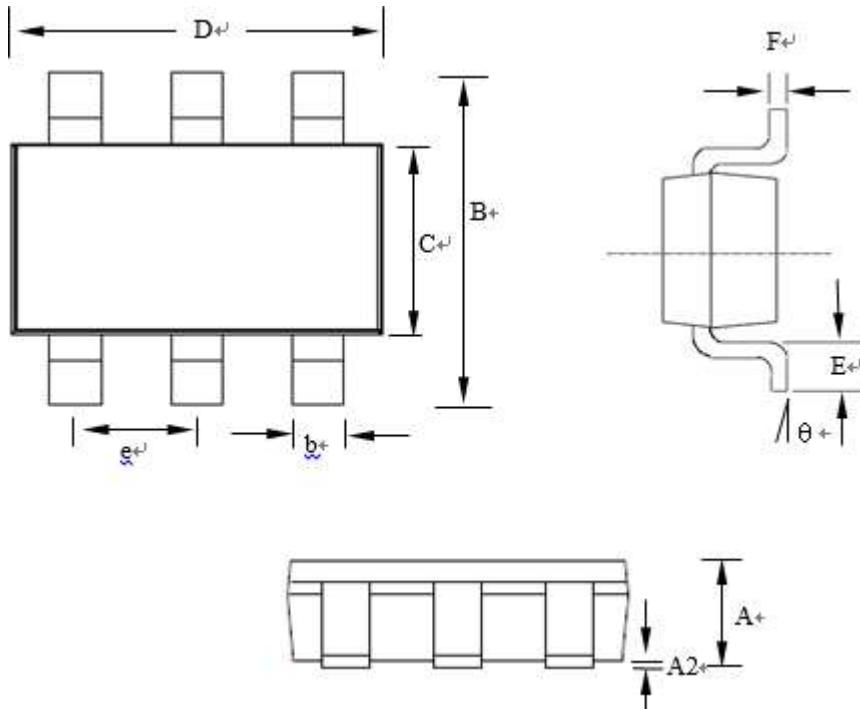


Profile Feature	Pb-Free Assembly Profile
Temperature Min. (T _{smin})	150°C
Temperature Max. (T _{smax})	200°C
Time (t _s) from (T _{smin} to T _{smax})	60-120 seconds
Average Ramp-up Rate (t _L to t _P)	3°C/second max.
Liquidous Temperature (T _L)	217°C
Time (t _L) Maintained Above (T _L)	60 – 150 seconds
Peak Temperature	260°C +0°C / -5°C
Time (t _P) within 5°C of actual Peak Temperature	30 seconds
Ramp-down Rate (T _P to T _L)	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.

➤ **Ordering Information**

Part Number	Description	Quantity
PAP31S11C	TSOP-6 Reel	3000 pcs

➤ **Package Information (TSOP-6)**



SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.70	--	0.9	0.028	--	0.035
A2	0.00	--	0.10	0.000	--	0.004
B	2.60	2.80	3.00	0.102	0.110	0.118
C	1.40	1.60	1.80	0.055	0.063	0.071
D	2.70	2.90	3.10	0.106	0.114	0.122
E	0.30	0.40	0.60	0.012	0.016	0.024
F	0.07	0.127	0.20	0.003	0.005	0.008
b	0.30	0.40	0.50	0.012	0.016	0.020
e	--	0.95	--	--	0.037	--
θ	0°	5°	10°	0°	5°	10°

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