

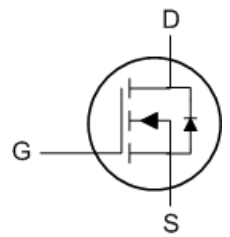
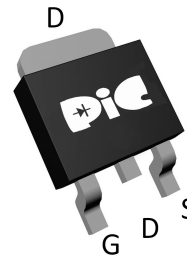
➤ General Description

This PAN60TX32X N-Channel enhancement mode power field effect transistor is the high density trench technology and this advanced technology can provide excellent $R_{ds(On)}$ performance and efficiency for power switching and load switching application., this device also comply with the RoHS and Green Product requirement with full function reliability approved.

➤ Feature

- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

➤ TO-252



➤ Application

- Switch application
- DC/DC Converters Power
- Tools

➤ Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current, V_{GS} @ 10V ₁	$I_D@T_C=25^\circ C$	75	A
Continuous Drain Current, V_{GS} @ 10V ₁	$I_D@T_C=100^\circ C$	47	A
Pulsed Drain Current ₂	I_{DM}	280	A
Single Pulse Avalanche Energy ₃	EAS	80	mJ
Avalanche Current	I_{AS}	40	A
Total Power Dissipation ₄	$P_D@T_C=25^\circ C$	41	W
Storage Temperature Range	T_{STG}	-55 to 150	$^\circ C$
Operating Junction Temperature Range	T_J	-55 to 150	$^\circ C$
Thermal Resistance Junction-ambient ₁	$R_{\theta JA}$	62	$^\circ C/W$
Thermal Resistance Junction-case ₁	$R_{\theta JC}$	1.4	$^\circ C/W$

➤ Electrical Characteristics ($T_J=25^\circ C$ Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V$, $I_D=250\mu A$	60	---	---	V
Static Drain-Source On-Resistance ²	$R_{DS(ON)}$	$V_{GS}=10V$, $I_D=20A$	---	7.1	8.5	$m\Omega$
		$V_{GS}=4.5V$, $I_D=15A$	---	9.5	12	$m\Omega$
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}$, $I_D=250\mu A$	1.2	---	2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=48V$, $V_{GS}=0V$, $T_J=25^\circ C$	---	---	1	μA
		$V_{DS}=48V$, $V_{GS}=0V$, $T_J=55^\circ C$	---	---	5	
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V$, $V_{DS}=0V$	---	---	± 100	nA
Gate Resistance	R_g	$V_{DS}=0V$, $V_{GS}=0V$, $f=1MHz$	---	1.2	---	Ω
Total Gate Charge (10V)	Q_g	$V_{DS}=30V$, $V_{GS}=10V$, $I_D=18A$	---	57	---	nC
Gate-Source Charge	Q_{gs}		---	8.7	---	
Gate-Drain Charge	Q_{gd}		---	14	---	
Turn-On Delay Time	$T_{d(on)}$	$V_{DD}=30V$, $V_{GS}=10V$, $R_G=3.3\Omega$, $I_D=20A$	---	16.2	---	ns
Rise Time	T_r		---	41.2	---	
Turn-Off Delay Time	$T_{d(off)}$		---	56.4	---	
Fall Time	T_f		---	16.2	---	
Input Capacitance	C_{iss}	$V_{DS}=30V$, $V_{GS}=0V$, $f=1MHz$	---	3307	---	pF
Output Capacitance	C_{oss}		---	201	---	
Reverse Transfer Capacitance	C_{rss}		---	151	---	

➤ Diode Characteristics

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Continuous Source Current ^{1,5}	I_S	$V_G=V_D=0V$, Force Current	---	---	75	A
Diode Forward Voltage ²	V_{SD}	$V_{GS}=0V$, $I_S=1A$, $T_J=25^\circ C$	---	---	1.2	V
Reverse Recovery Time	t_{rr}	$I_F=20A$, $dI/dt=100A/\mu s$,	---	22	---	nS
Reverse Recovery Charge	Q_{rr}	$T_J=25^\circ C$	---	72	---	

Note :

1. Pulse width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
3. The EAS data shows Max. rating. The test condition is $V_{DD}=50V$, $V_{GS}=10V$, $L=0.1mH$, $I_{AS}=40A$
4. Ensure that the channel temperature does not exceed $150^\circ C$.
5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

➤ Typical Characteristics

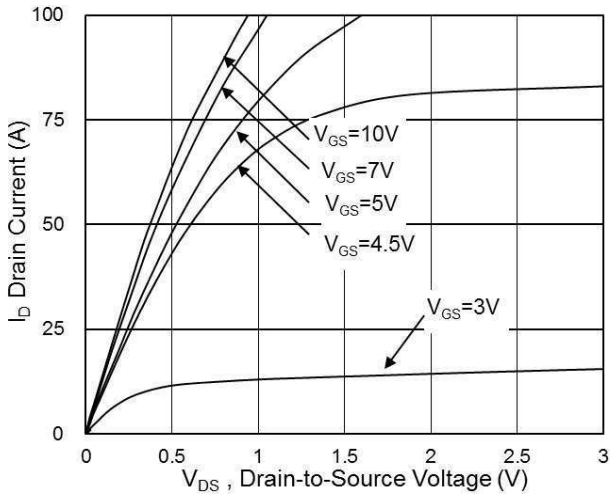


Fig.1 Typical Output Characteristics

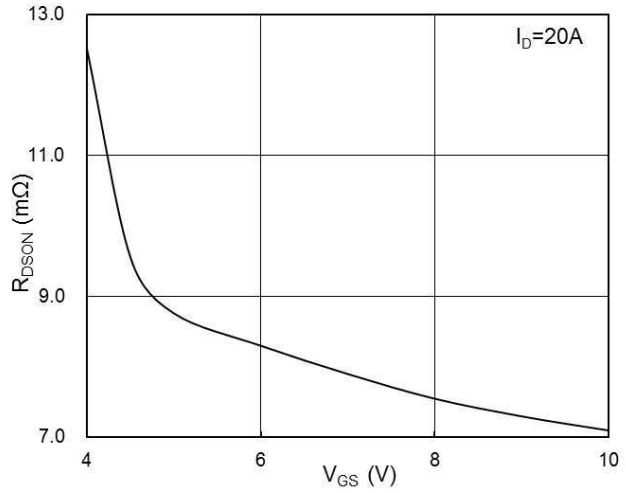


Fig.2 On-Resistance vs Gate-Source Voltage

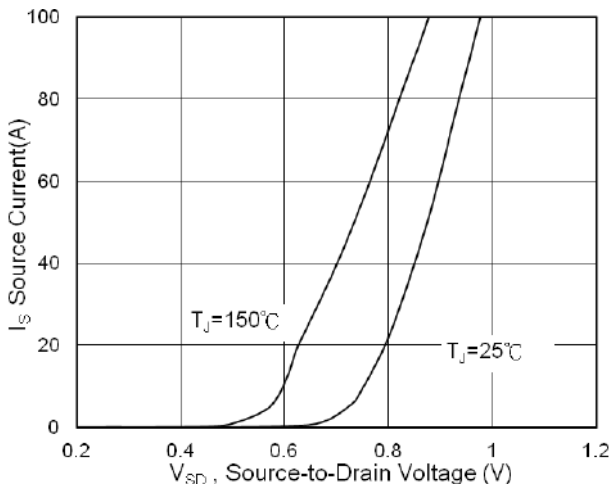


Fig.3 Forward Characteristics of Reverse

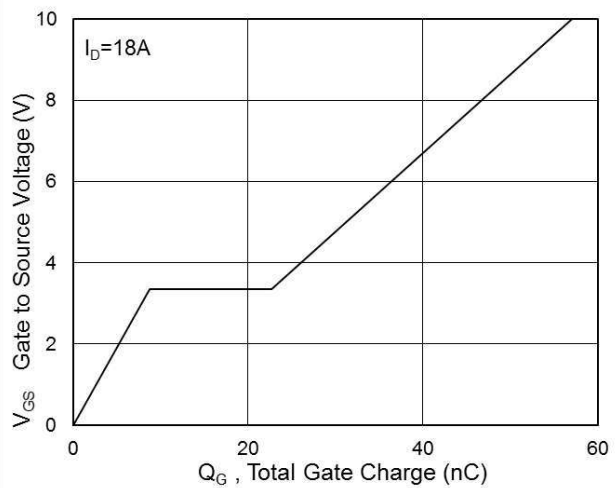


Fig.4 Gate-Charge Characteristics

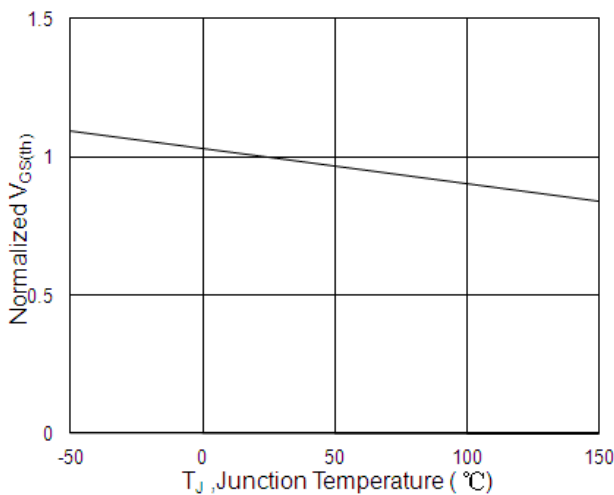


Fig.5 Normalized $V_{GS(th)}$ vs T_J

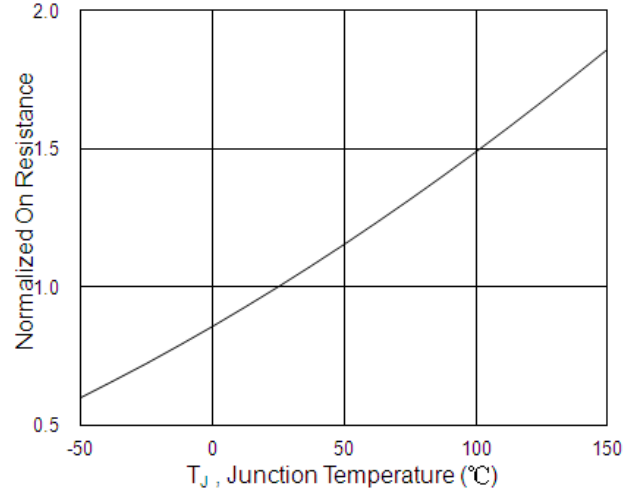


Fig.6 Normalized $R_{DS(ON)}$ vs T_J

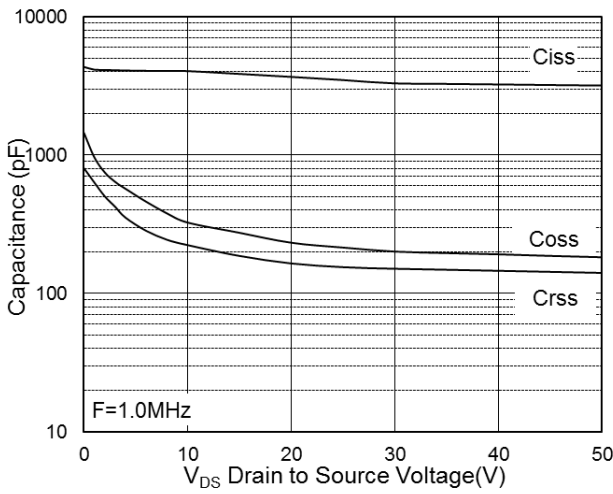


Fig.7 Capacitance

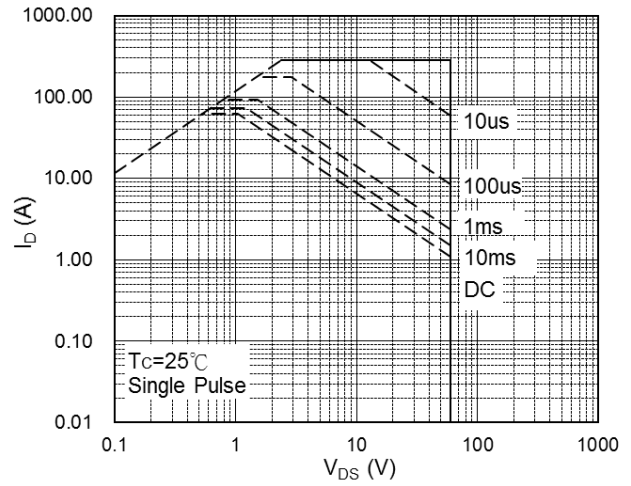


Fig.8 Safe Operating Area

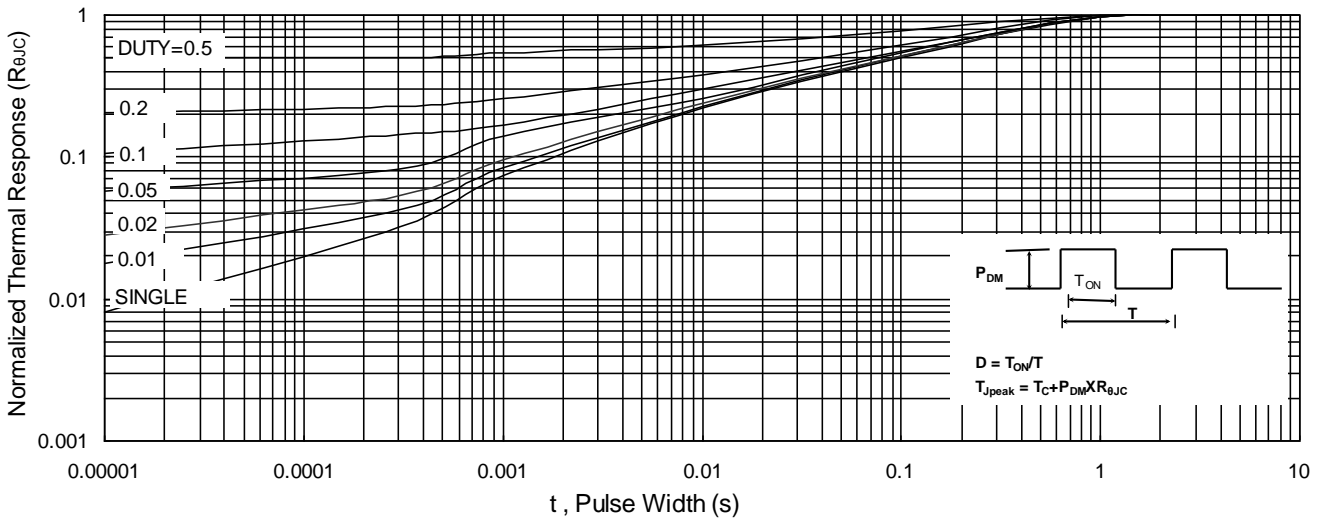


Fig.9 Normalized Maximum Transient Thermal Impedance

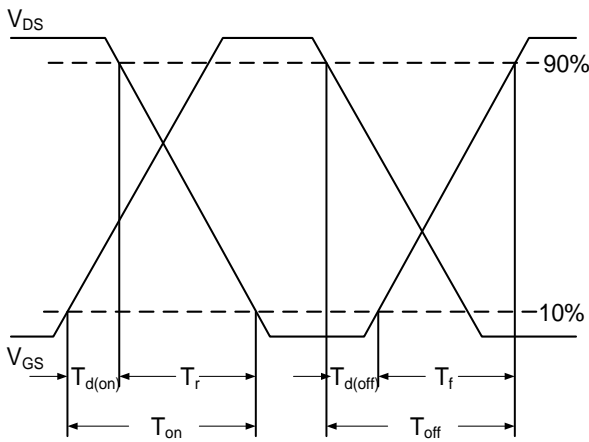


Fig.10 Switching Time Waveform

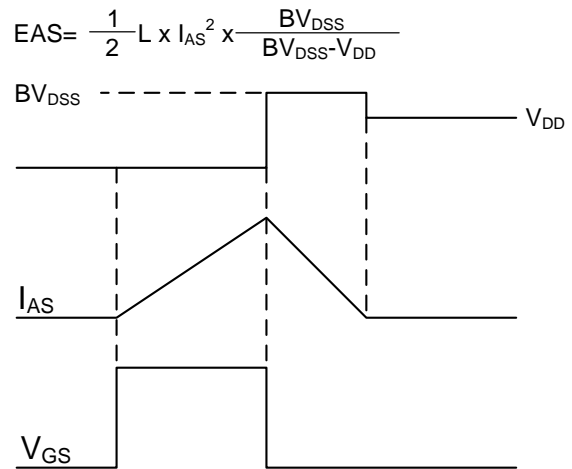


Fig.11 Unclamped Inductive Switching Waveform

➤ Recommand IR Reflow Soldering Thermal Profile

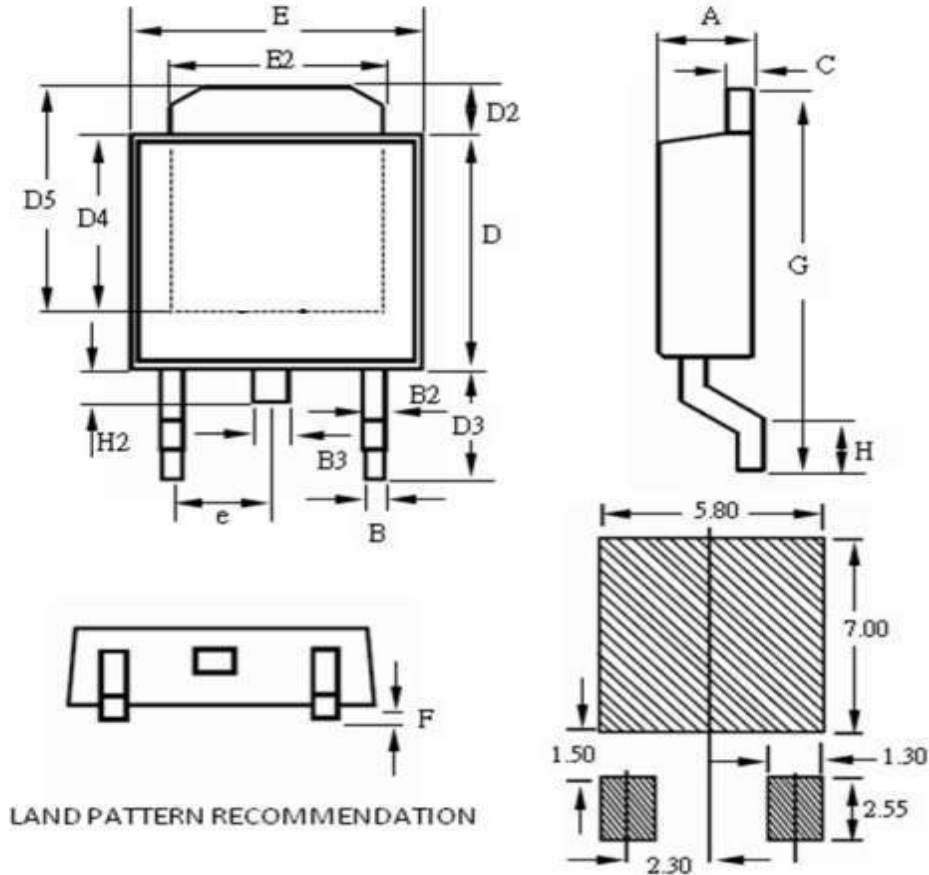


Profile Feature	Pb-Free Assembly Profile
Temperature Min. (T _{smin})	150°C
Temperature Max. (T _{smax})	200°C
Time (t _s) from (T _{smin} to T _{smax})	60-120 seconds
Average Ramp-up Rate (t _L to t _P)	3°C/second max.
Liquidous Temperature (T _L)	217°C
Time (t _L) Maintained Above (T _L)	60 – 150 seconds
Peak Temperature	260°C +0°C / -5°C
Time (t _P) within 5°C of actual Peak Temperature	30 seconds
Ramp-down Rate (T _P to T _L)	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.

➤ Ordering Information

Part Number	Description	Quantity
PAN60TX32X	TO-252 Reel	2500 pcs

➤ Package Information (TO-252)



LAND PATTERN RECOMMENDATION

SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	2.10	--	2.50	0.083	--	0.098
B	0.30	--	0.89	0.012	--	0.035
B2	0.40	--	1.14	0.016	--	0.045
B3	0.60	--	1.00	0.024	--	0.039
C	0.40	--	0.89	0.016	--	0.035
D	5.30	--	6.25	0.209	--	0.246
D2	0.50	--	1.70	0.020	--	0.067
D3	2.20	--	3.40	0.087	--	0.134
D4	4.32	--	--	0.170	--	--
D5	5.21	--	--	0.205	--	--
E	6.30	--	6.73	0.248	--	0.265
E2	4.80	--	5.46	0.189	--	0.215
F	0.00	--	0.30	0.000	--	0.012
G	9.20	--	10.41	0.362	--	0.410
H	0.90	--	1.95	0.035	--	0.077
H2	0.50	--	1.10	0.020	--	0.043
e	--	2.30	--	--	0.091	--

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