

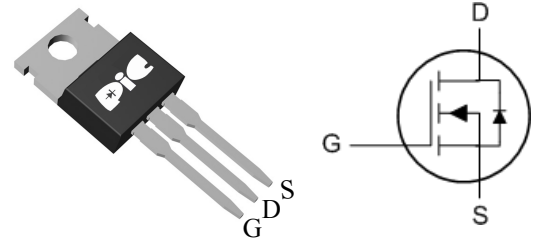
➤ General Description

This PAN60TG04G N-Channel enhancement mode power field effect transistor is the high density trench technology and this advanced technology can provide excellent $R_{ds(On)}$ performance and efficiency for power switching and load switching application., this device also comply with the RoHS and Green Product requirement with full function reliability approved.

➤ Feature

- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology
- TO-220 package design

➤ TO-220



➤ Application

- SMPS Power Supplier.
- Charger Adapter
- Power Tools
- LED Lighting

➤ Absolute Maximum Ratings

Parameter	Symbol	Rating	its
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 2	V
Continuous Drain Current, $V_{GS} @ 10V_1$	$I_D @ T_C=25^\circ C$	30	A
Continuous Drain Current, $V_{GS} @ 10V_1$	$I_D @ T_C=100^\circ C$	19	A
Continuous Drain Current, $V_{GS} @ 10V_1$	$I_D @ T_A=25^\circ C$	5.6	A
Continuous Drain Current, $V_{GS} @ 10V_1$	$I_D @ T_A=70^\circ C$	4.5	A
Pulsed Drain Current ²	I_{DM}	70	A
Single Pulse Avalanche Energy ³	EAS	34.5	mJ
Avalanche Current	I_{AS}	22.6	A
Total Power Dissipation ⁴	$P_D @ T_C=25^\circ C$	58	W
Total Power Dissipation ⁴	$P_D @ T_A=25^\circ C$	2	W
Storage Temperature Range	T_{STG}	-55 to 150	$^\circ C$
Operating Junction Temperature Range	T_J	-55 to 150	$^\circ C$
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	62	$^\circ C/W$
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	2.16	$^\circ C/W$

➤ Electrical Characteristics ($T_J=25^\circ C$ Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	60	---	---	V
BV_{DSS} Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_J$	Reference to $25^\circ C, I_D=1mA$	---	0.063	---	$V/^\circ C$
Static Drain-Source On-Resistance ²	$R_{DS(ON)}$	$V_{GS}=10V, I_D=12A$	---	---	30	m Ω
		$V_{GS}=4.5V, I_D=10A$	---	---	38	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	---	2.5	V
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}$		---	-5.24	---	$mV/^\circ C$
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=48V, V_{GS}=0V, T_J=25^\circ C$	---	---	1	μA
		$V_{DS}=48V, V_{GS}=0V, T_J=55^\circ C$	---	---	5	
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
Forward Transconductance	g_{fs}	$V_{DS}=5V, I_D=12A$	---	17	---	S
Gate Resistance	R_g	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	---	3.2	6.4	Ω
Total Gate Charge (4.5V)	Q_g	$V_{DS}=48V, V_{GS}=4.5V, I_D=10A$	---	12.6	---	nC
Gate-Source Charge	Q_{gs}		---	3.2	---	
Gate-Drain Charge	Q_{gd}		---	6.3	---	
Turn-On Delay Time	$T_{d(on)}$	$V_{DD}=30V, V_{GS}=10V, R_G=3.3\Omega, I_D=10A$	---	8	---	ns
Rise Time	T_r		---	14.2	---	
Turn-Off Delay Time	$T_{d(off)}$		---	24.4	---	
Fall Time	T_f		---	4.6	---	
Input Capacitance	C_{iss}	$V_{DS}=15V, V_{GS}=0V, f=1MHz$	---	1378	---	pF
Output Capacitance	C_{oss}		---	86	---	
Reverse Transfer Capacitance	C_{rss}		---	64	---	

➤ Diode Characteristics

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Continuous Source Current ^{1,5}	I_S	$V_G=V_D=0V, \text{Force Current}$	---	---	30	A
Pulsed Source Current ^{2,5}	I_{SM}		---	---	60	A
Diode Forward Voltage ²	V_{SD}	$V_{GS}=0V, I_S=1A, T_J=25^\circ C$	---	---	1.2	V

Note :

- Pulse width limited by maximum junction temperature.
- The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The EAS data shows Max. rating . The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=22.6A$
- Ensure that the channel temperature does not exceed $150^\circ C$.
- The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

➤ Typical Characteristics

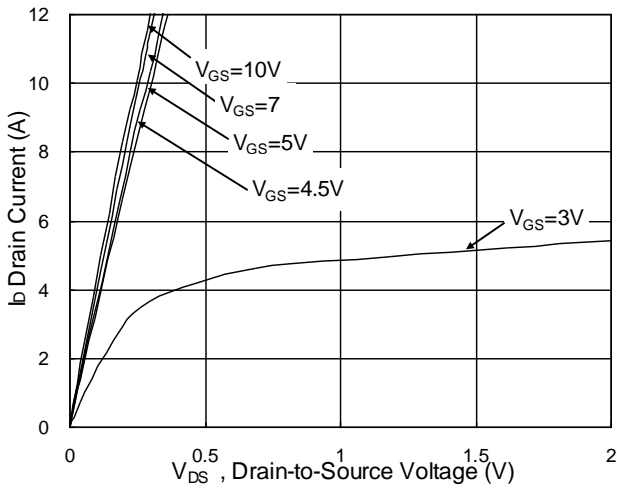


Fig.1 Typical Output Characteristics

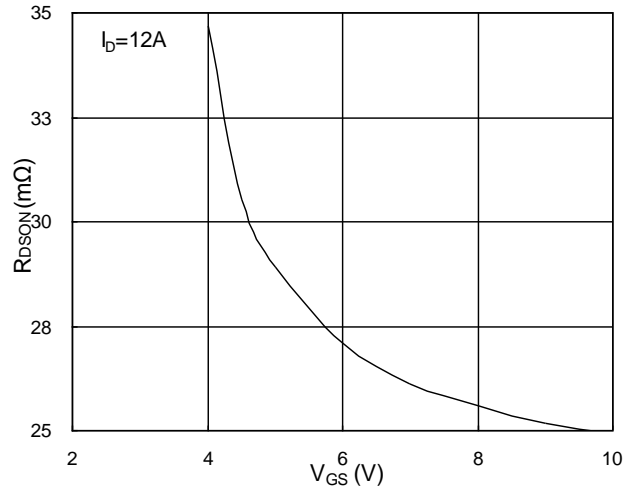
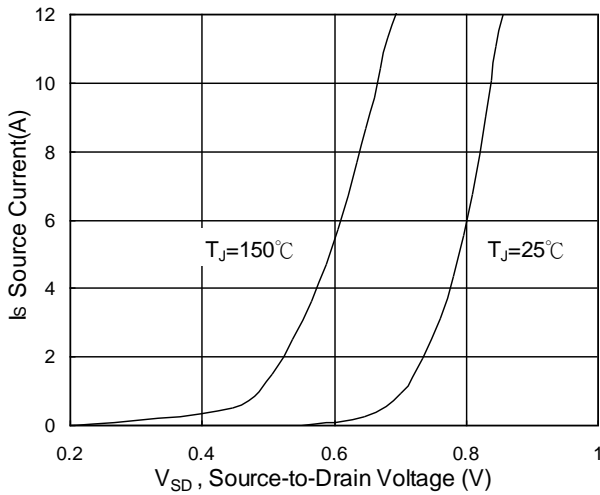


Fig.2 On-Resistance v.s Gate-Source



Forward Characteristics of Reverse

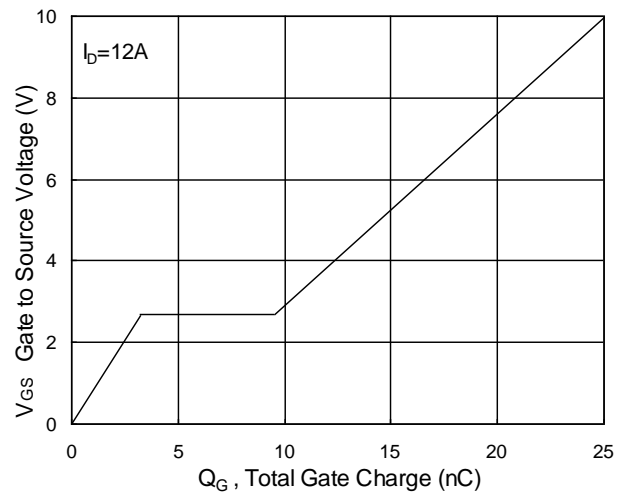


Fig.4 Gate-Charge Characteristics

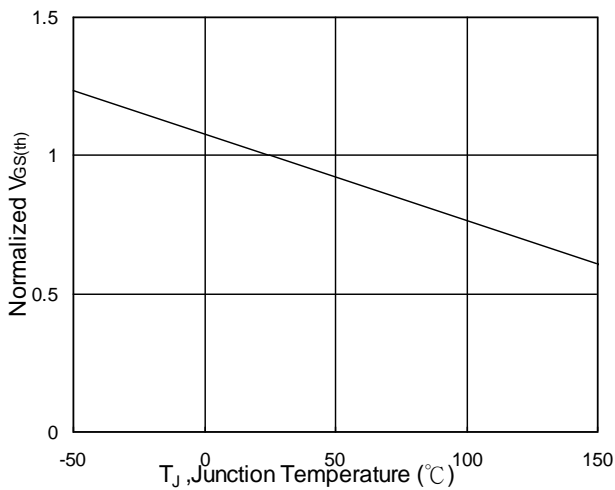


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

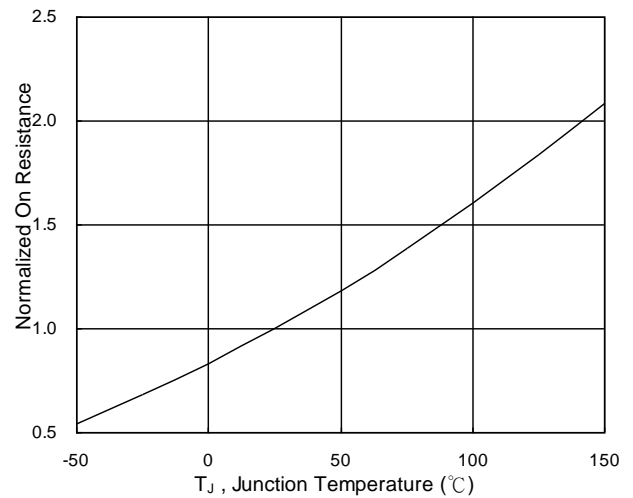


Fig.6 Normalized $R_{DS(on)}$ v.s T_J

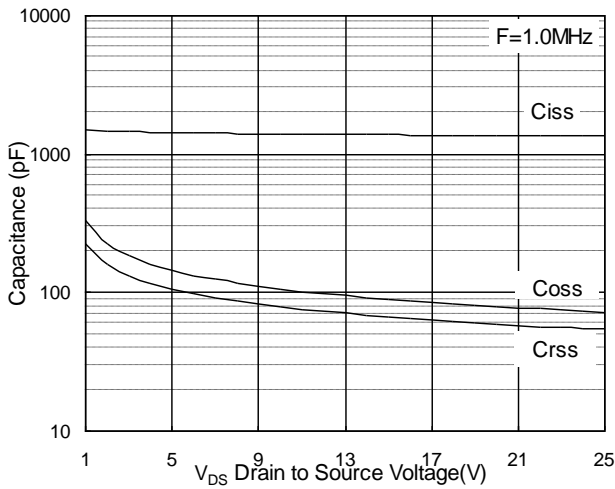


Fig.7 Capacitance

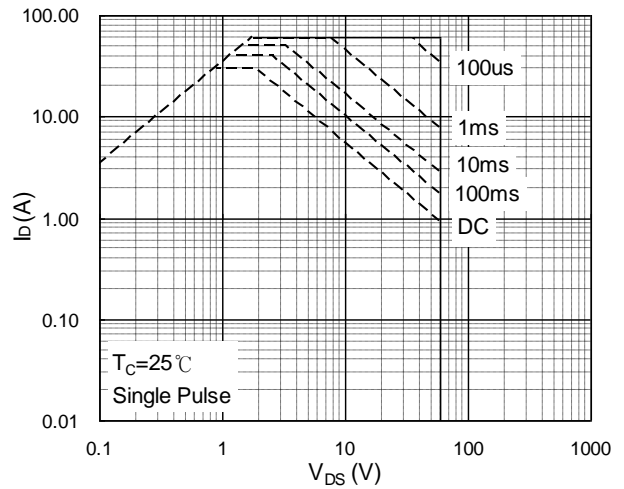


Fig.8 Safe Operating Area

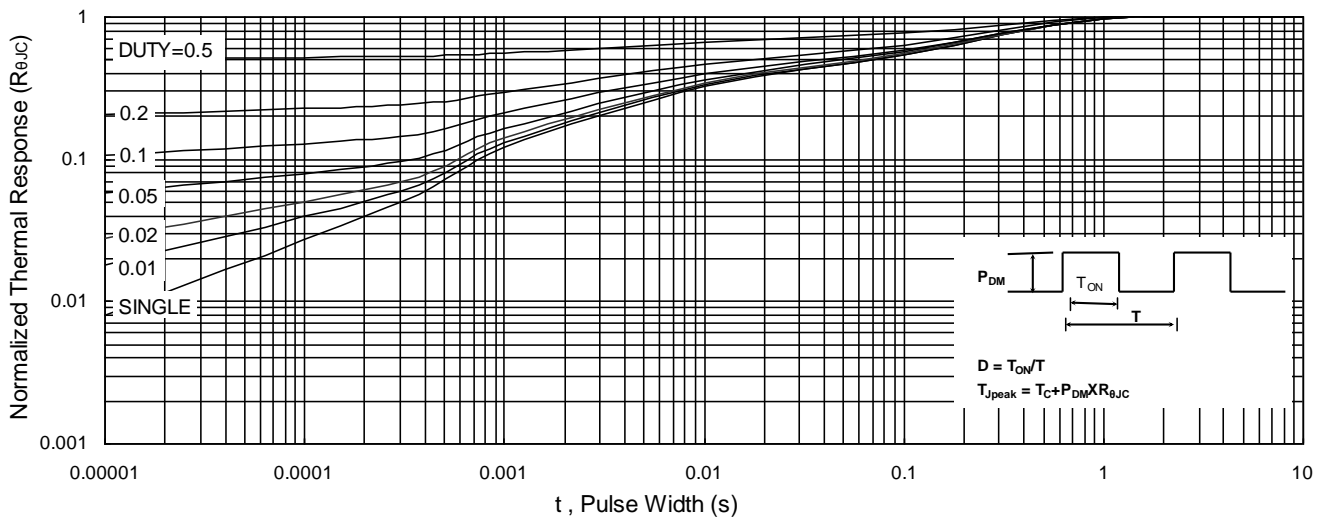


Fig.9 Normalized Maximum Transient Thermal Impedance

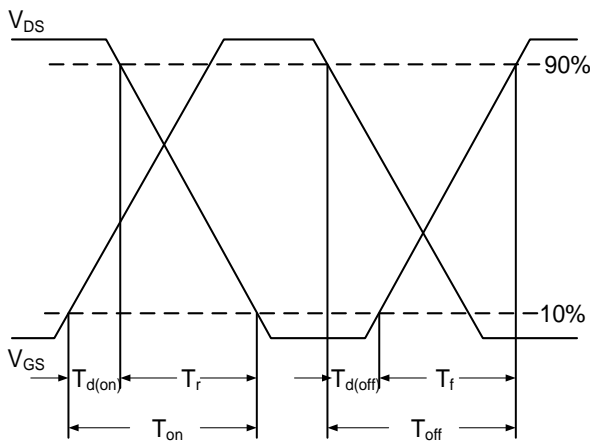


Fig.10 Switching Time Waveform

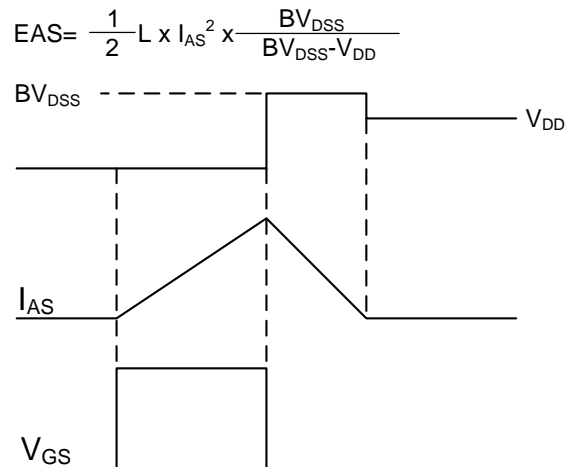


Fig.11 Unclamped Inductive Waveform

➤ Recommand IR Reflow Soldering Thermal Profile

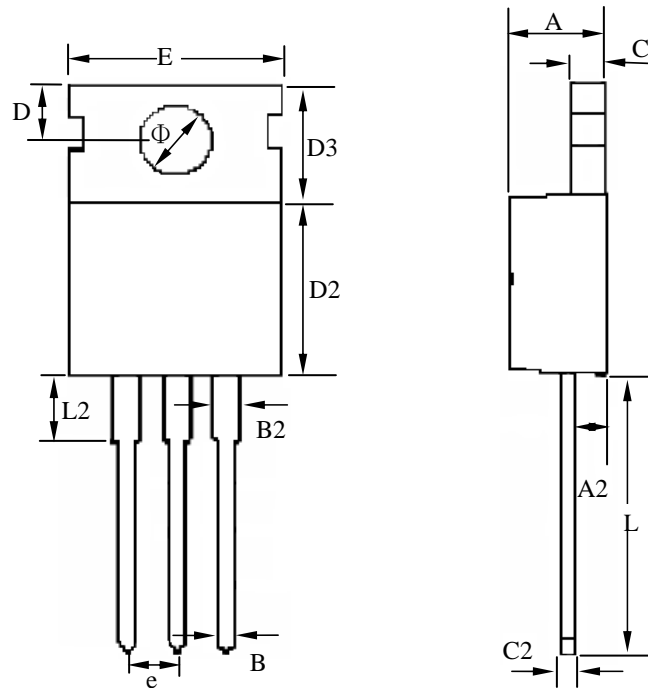


Profile Feature	Pb-Free Assembly Profile
Temperature Min. (T _{smin})	150°C
Temperature Max. (T _{smax})	200°C
Time (t _s) from (T _{smin} to T _{smax})	60-120 seconds
Average Ramp-up Rate (t _L to t _P)	3°C/second max.
Liquidous Temperature (T _L)	217°C
Time (t _L) Maintained Above (T _L)	60 – 150 seconds
Peak Temperature	260°C +0°C / -5°C
Time (t _P) within 5°C of actual Peak Temperature	30 seconds
Ramp-down Rate (T _P to T _L)	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.

➤ Ordering Information

Part Number	Description	Quantity
PAN60TG04G	TO-220	50 pcs/tube

➤ Package Information (TO-220)



SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	4.25	--	4.80	0.167	--	0.189
A2	2.20	--	2.92	0.087	--	0.115
B	0.70	--	0.91	0.028	--	0.036
B2	1.15	--	1.77	0.045	--	0.070
C	1.20	--	1.40	0.047	--	0.055
C2	0.45	--	0.61	0.018	--	0.024
D	2.54	--	3.00	0.100	--	0.118
D2	8.39	--	9.47	0.330	--	0.373
D3	6.30	--	6.70	0.248	--	0.264
E	9.70	--	10.36	0.382	--	0.408
L	12.75	--	14.40	0.502	--	0.567
L2	2.45	--	4.05	0.096	--	0.159
Φ	3.50	--	3.80	0.138	--	0.150
e	--	2.54	--	--	0.100	--

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