

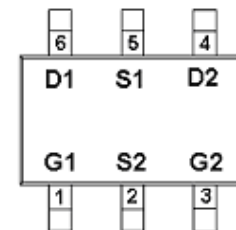
➤ General Description

This PAN3062C N-Channel enhancement mode power field effect transistor is the high density trench technology and this advanced technology can provide excellent $R_{ds(On)}$ performance and efficiency for power switching and load switching application., this device also comply with the RoHS and Green Product requirement with full function reliability approved.

➤ Feature

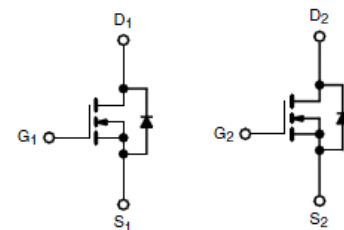
- Super high density cell design for extremely low $R_{DS(ON)}$
- TSOP-6 package design

➤ TSOP-6



➤ Application

- Power Management in Notebook
- LED Display
- DC-DC System
- LCD Panel



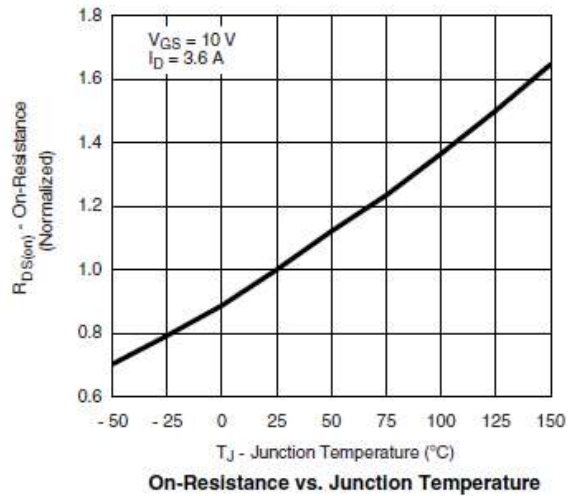
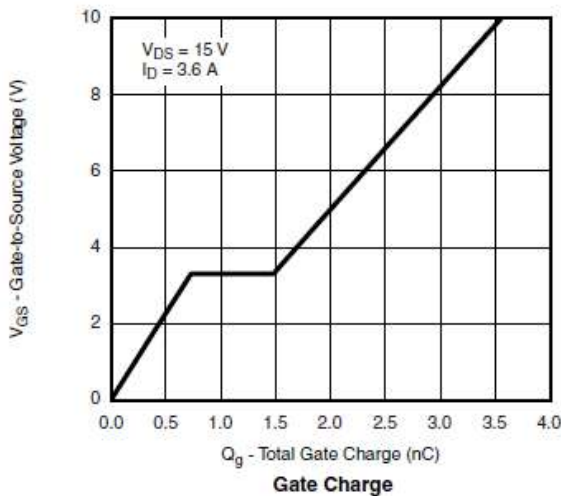
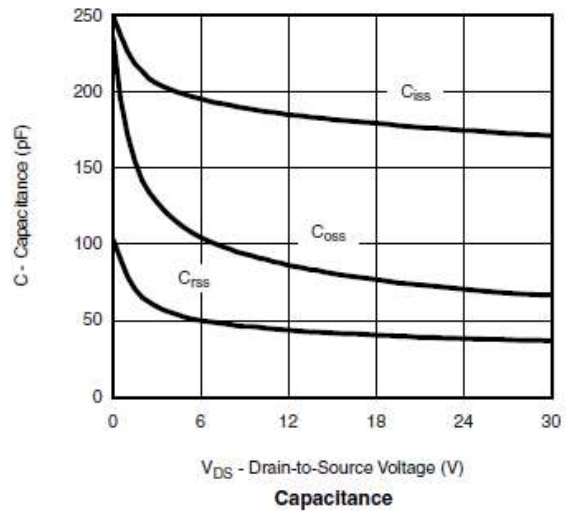
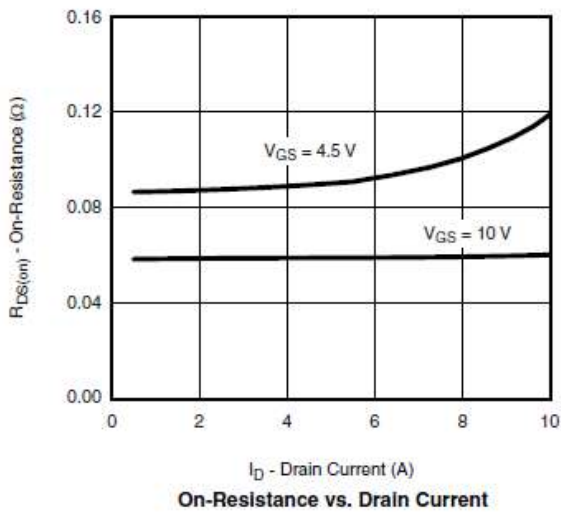
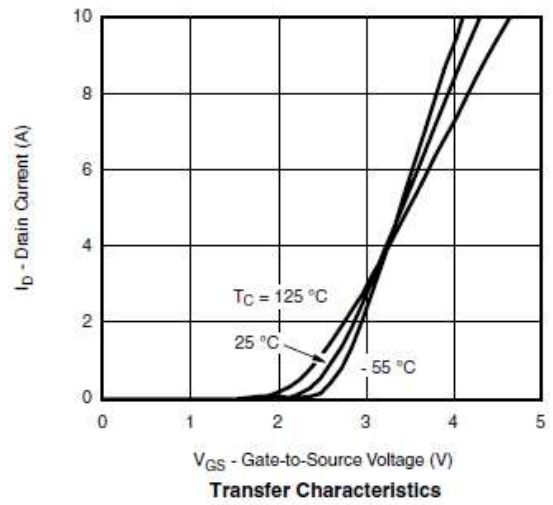
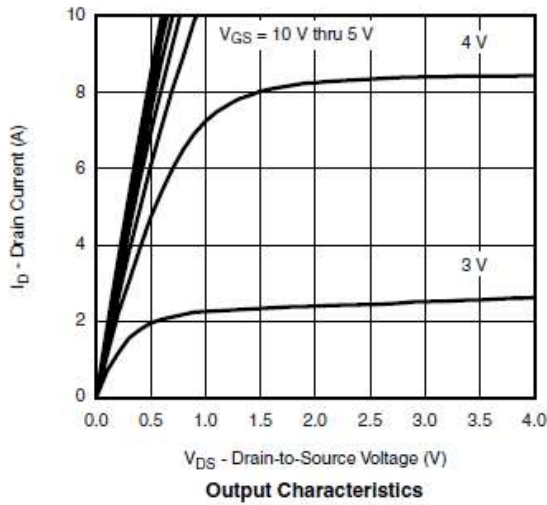
➤ Absolute Maximum Ratings

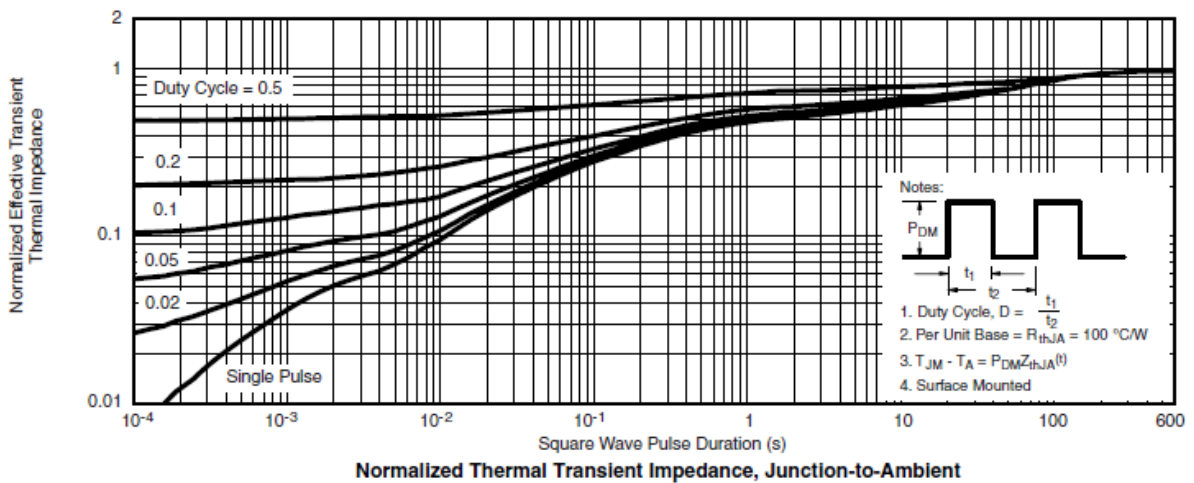
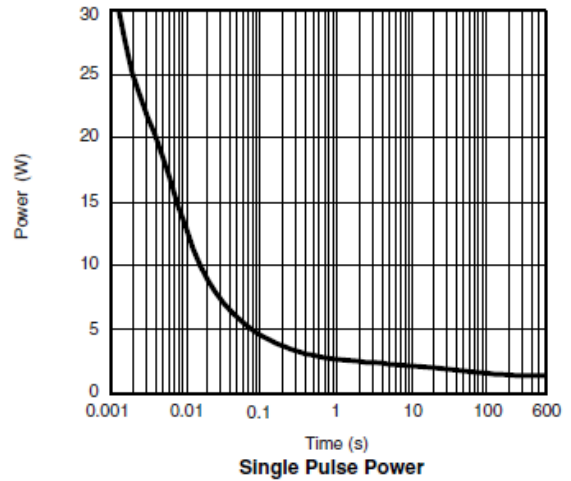
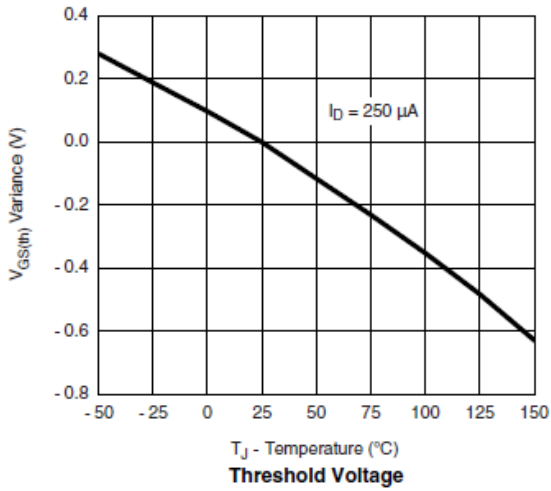
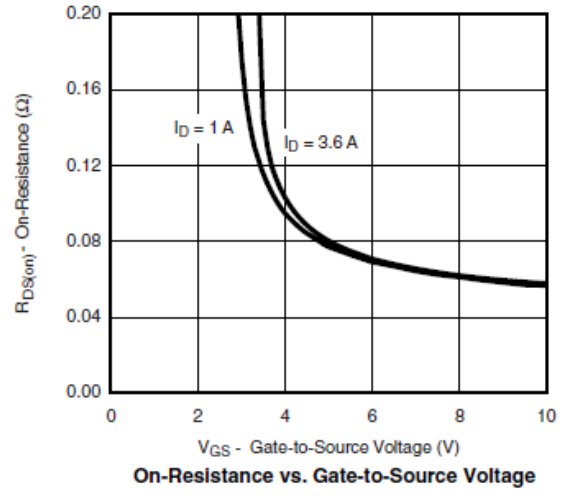
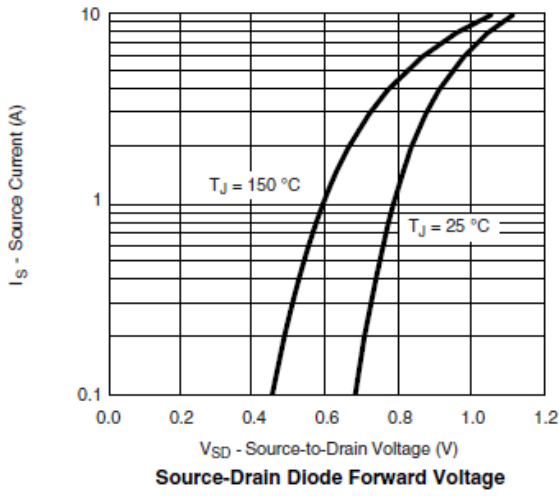
Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DSS}	30	V
Gate –Source Voltage	V_{GSS}	± 12	V
Continuous Drain Current($T_J=150^\circ C$)	I_D	$T_A=25^\circ C$	3.6
		$T_A=70^\circ C$	2.2
Pulsed Drain Current	I_{DM}	20	A
Continuous Source Current(Diode Conduction)	I_S	1.7	A
Power Dissipation	P_D	$T_A=25^\circ C$	2.0
		$T_A=70^\circ C$	1.3
Operating Junction Temperature	T_J	150	$^\circ C$
Storage Temperature Range	T_{STG}	-55/150	$^\circ C$
Thermal Resistance-Junction to Ambient	$R_{\theta JA}$	120	$^\circ C/W$

➤ **Electrical Characteristics (T_A=25°C Unless otherwise noted)**

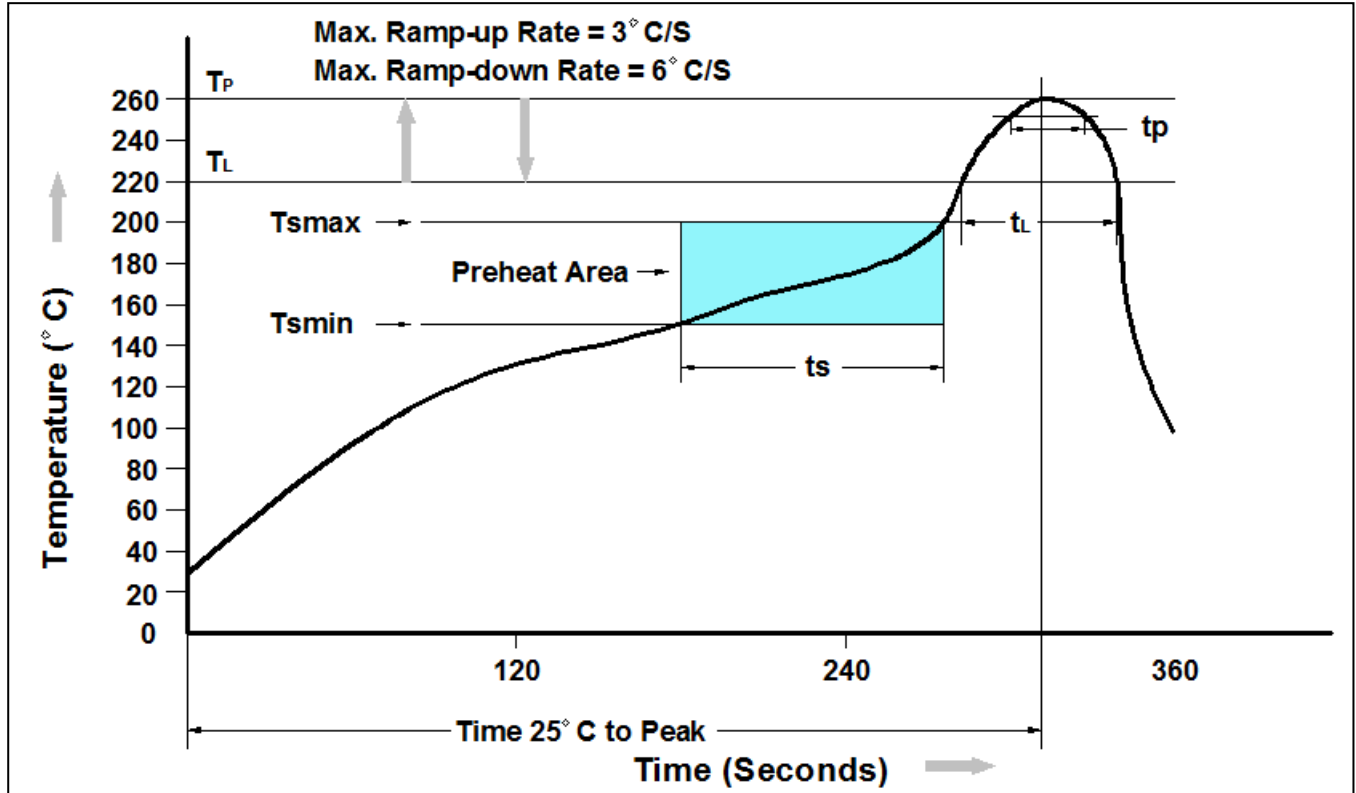
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.4		1.2	
Gate Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 12V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=24V, V_{GS}=0V$			1	uA
		$V_{DS}=24V, V_{GS}=0V$ $T_J=85^\circ C$			30	
On-State Drain Current	$I_{D(on)}$	$V_{DS} \geq 5V, V_{GS}=4.5V$	30			A
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=3.6A$		56	70	mΩ
		$V_{GS}=4.5V, I_D=3.0A$		62	78	
		$V_{GS}=2.5V, I_D=2.2A$		110	145	
Forward Transconductance	g_{FS}	$V_{DS}=10V, I_D=1.6A$		20		S
Diode Forward Voltage	V_{SD}	$I_S=1.7A, V_{GS}=0V$		0.8	1.2	V
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=15V, V_{GS}=4.5V$ $I_D=3.6A$		2.3	3	nC
Gate-Source Charge	Q_{gs}			1.0		
Gate-Drain Charge	Q_{gd}			0.6		
Input Capacitance	C_{iss}	$V_{DS}=15V, V_{GS}=0V$ $f=1MHz$		280		pF
Output Capacitance	C_{oss}			40		
Reverse Transfer Capacitance	C_{rss}			20		
Turn-On Time	$t_{d(on)}$	$V_{DD}=15V, R_L=15\Omega$ $I_D=1.0A, V_{GEN}=10V$ $R_G=6\Omega$		10	15	ns
	t_r			12	20	
Turn-Off Time	$t_{d(off)}$			15	25	
	t_f			10	15	

➤ Typical Characteristics





➤ Recommnd IR Reflow Soldering Thermal Profile

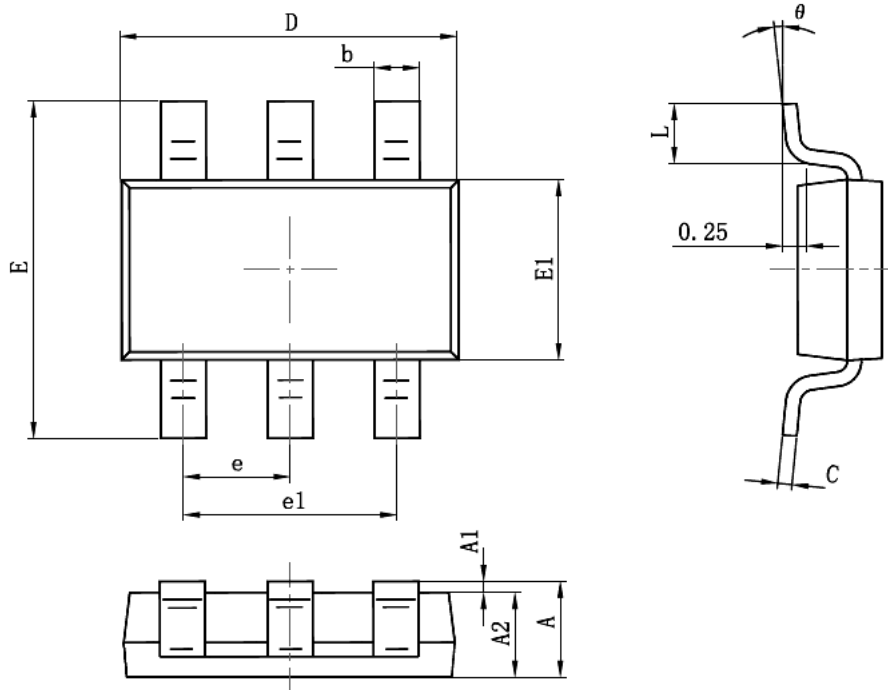


Profile Feature	Pb-Free Assembly Profile
Temperature Min. (T _{sm})	150°C
Temperature Max. (T _{sm})	200°C
Time (t _s) from (T _{sm} to T _{sm})	60-120 seconds
Average Ramp-up Rate (t _L to t _P)	3°C/second max.
Liquidous Temperature (T _L)	217°C
Time (t _L) Maintained Above (T _L)	60 – 150 seconds
Peak Temperature	260°C +0°C / -5°C
Time (t _P) within 5°C of actual Peak Temperature	30 seconds
Ramp-down Rate (T _P to T _L)	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.

➤ Ordering Information

Part Number	Description	Quantity
PAN3062C	TSOP-6 Reel	3000 pcs

➤ Package Information (TSOP-6)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	---	0.900	---	0.035
A1	0.000	0.100	0.000	0.004
A2	0.700	0.800	0.028	0.031
b	0.350	0.500	0.014	0.020
c	0.080	0.200	0.003	0.008
D	2.820	3.020	0.111	0.119
E1	1.600	1.700	0.063	0.067
E	2.650	2.950	0.104	0.116
e	0.95 (BSC)		0.037(BSC)	
e1	1.90 (BSC)		0.075(BSC)	
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

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