

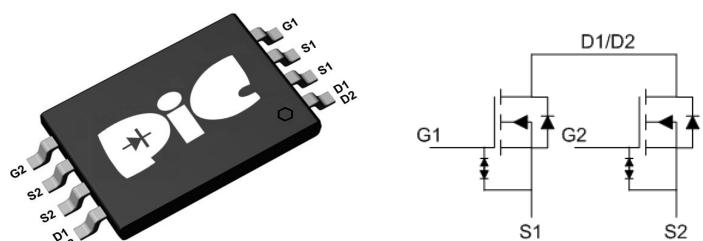
➤ General Description

This PAN27TE42JS Dual N-Channel enhancement mode power field effect transistor is the high density trench technology and this advanced technology can provide excellent Rds(On) performance and efficiency for power switching and load switching application., this device also comply with the RoHS and Green Product requirement with full function reliability approved.

➤ Feature

- Small footprint and thin package
- Low drain-source ON resistance
- Low leakage current
- TSSOP-8 package design

➤ TSSOP-8



➤ Application

- Lithium Ion Battery Applications
- Notebook PC Applications
- Portable Equipment Applications

➤ Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current ¹	$I_D @ T_A=25^\circ C$	6	A
Continuous Drain Current ¹	$I_D @ T_A=70^\circ C$	4.8	A
Pulsed Drain Current ²	I_{DM}	24	A
Total Power Dissipation ³	$P_D @ T_A=25^\circ C$	1.5	W
Storage Temperature Range	T_{STG}	-55 to 150	°C
Operating Junction Temperature Range	T_J	-55 to 150	°C
Thermal Resistance Junction-ambient ¹	$R_{\theta JA}$	85	°C/W

➤ **Electrical Characteristics (T_J=25°C Unless otherwise noted)**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V , I _D =250uA	20	---	---	V
Static Drain-Source On-Resistance ²	R _{DS(ON)}	V _{GS} =4.5V , I _D =5A	16.5	19	25	mΩ
		V _{GS} =4.0V , I _D =5A	17	19.5	26	
		V _{GS} =3.7V , I _D =5A	17.5	20	28.5	
		V _{GS} =3.1V , I _D =4A	19	21.5	32	
		V _{GS} =2.5V , I _D =4A	22	24	40	
Gate Threshold Voltage	V _{GS(th)}	V _{GS} =V _{DS} , I _D =250uA	0.5	---	1.2	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =16V , V _{GS} =0V , T _J =25°C	---	---	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±8V , V _{DS} =0V	---	---	±100	nA
Forward Transconductance	g _{fs}	V _{DS} =5V , I _D =3.5A	---	20	---	S
Total Gate Charge (4.5V)	Q _g	V _{DS} =15V , V _{GS} =4.5V , I _D =7A	---	11.4	---	nC
Gate-Source Charge	Q _{gs}		---	1.6	---	
Gate-Drain Charge	Q _{gd}		---	2.9	---	
Turn-On Delay Time	T _{d(on)}	V _{DD} =10V , V _{GS} =4.5V , R _G =3.3Ω I _D =5A	---	5	---	ns
Rise Time	T _r		---	32.4	---	
Turn-Off Delay Time	T _{d(off)}		---	28	---	
Fall Time	T _f		---	9	---	
Input Capacitance	C _{iss}	V _{DS} =15V , V _{GS} =0V , f=1MHz	---	863	---	pF
Output Capacitance	C _{oss}		---	87	---	
Reverse Transfer Capacitance	C _{rss}		---	71	---	

➤ **Diode Characteristics**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Continuous Source Current ^{1,4}	I _s	V _G =V _D =0V , Force Current	---	---	6	A
Diode Forward Voltage ²	V _{SD}	V _{GS} =0V , I _s =1A , T _J =25°C	---	---	1.2	V

Note :

- 1.Pulse width limited by maximum junction temperature.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.Ensure that the channel temperature does not exceed 150°C.
- 4.The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

➤ Typical Characteristics

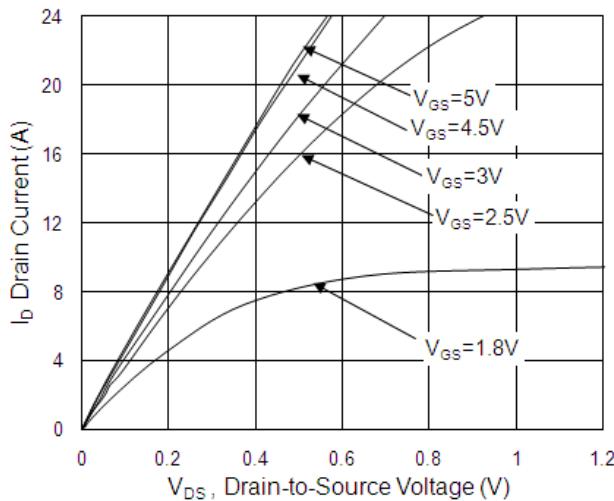


Fig.1 Typical Output Characteristics

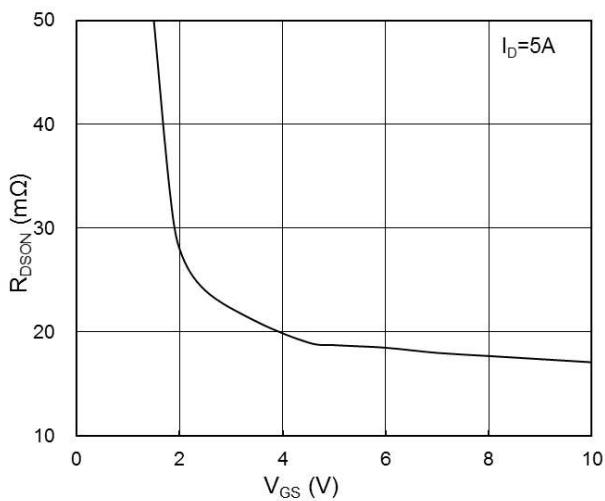


Fig.2 On-Resistance vs. Gate-Source Voltage

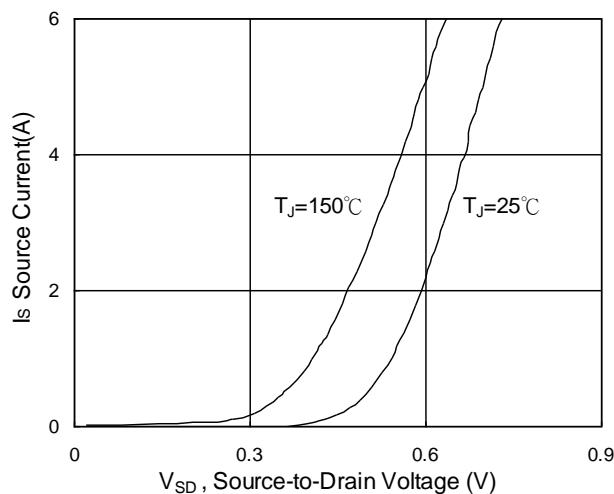


Fig.3 Forward Characteristics of Reverse

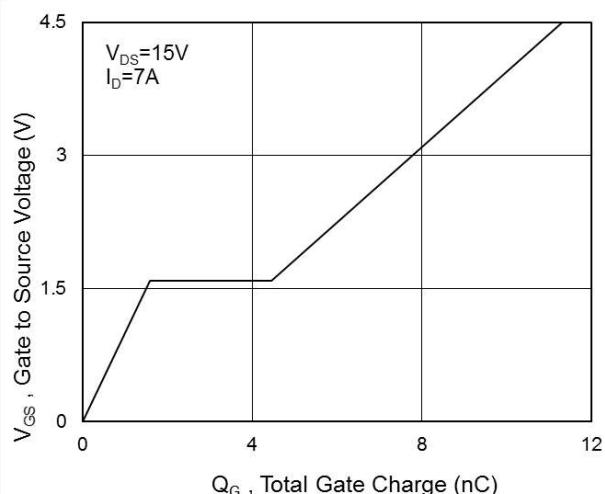


Fig.4 Gate-Charge Characteristics

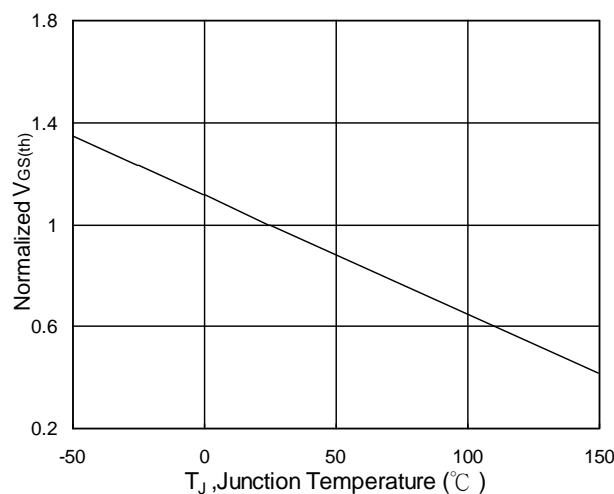


Fig.5 $V_{GS(th)}$ vs. T_J

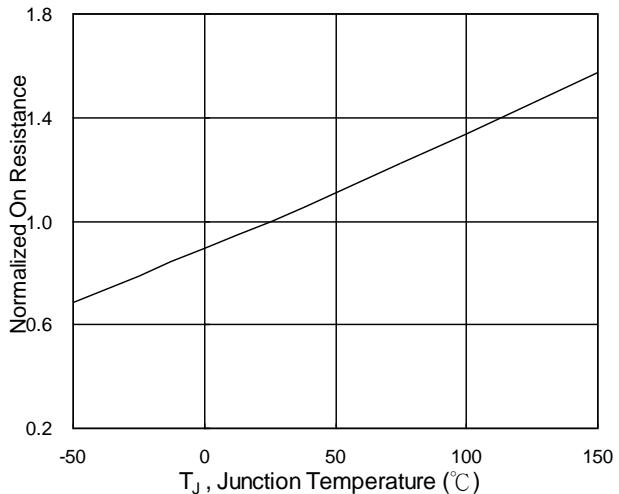
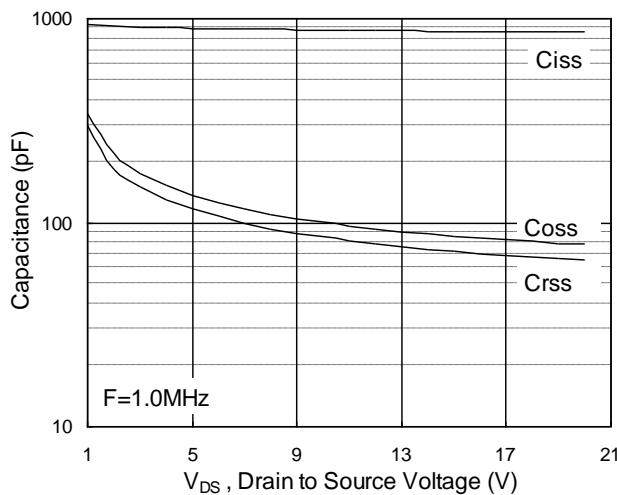
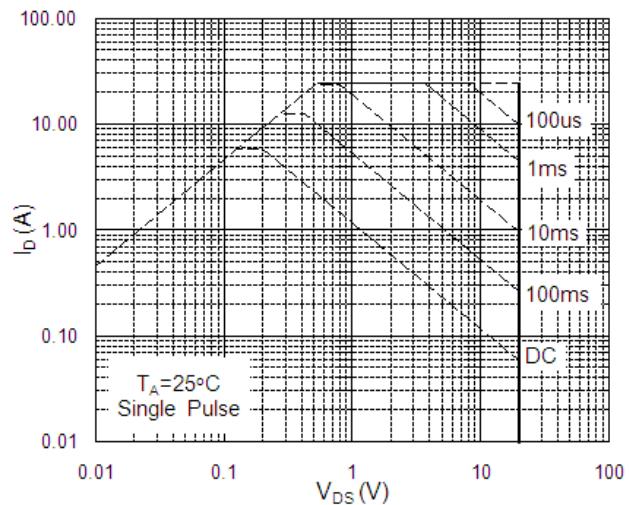
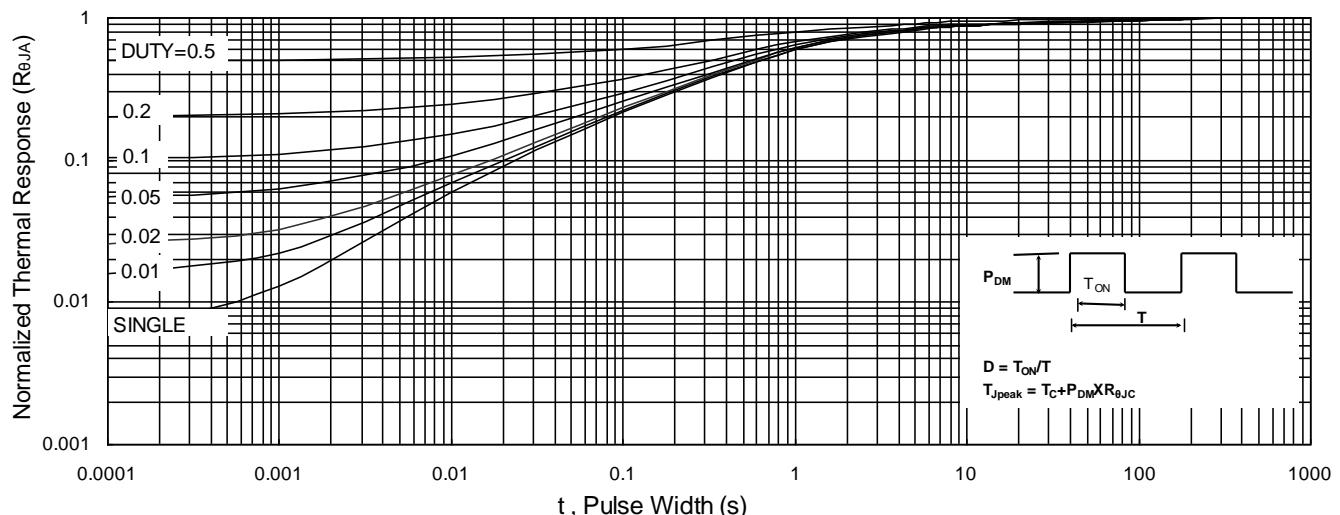
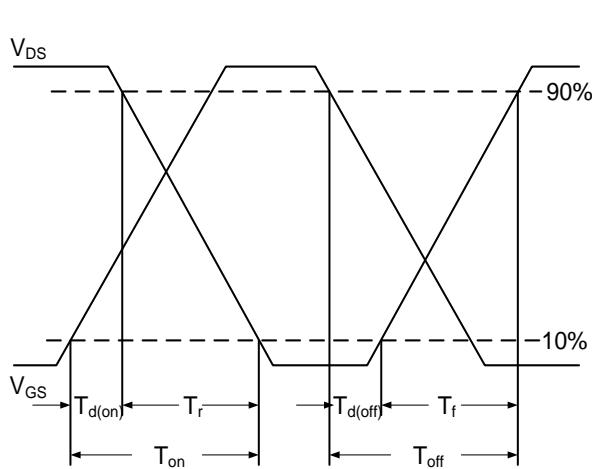
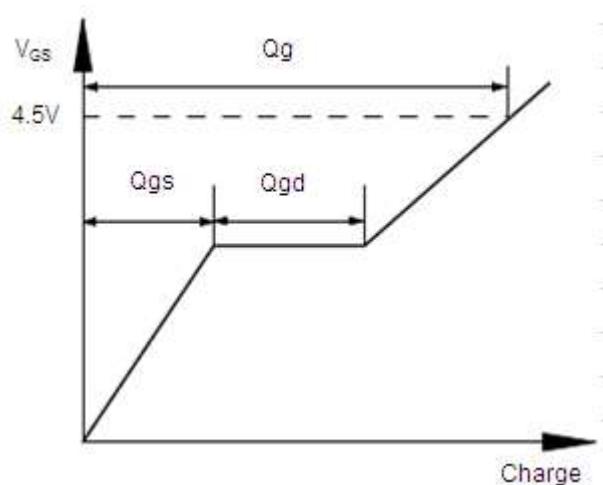
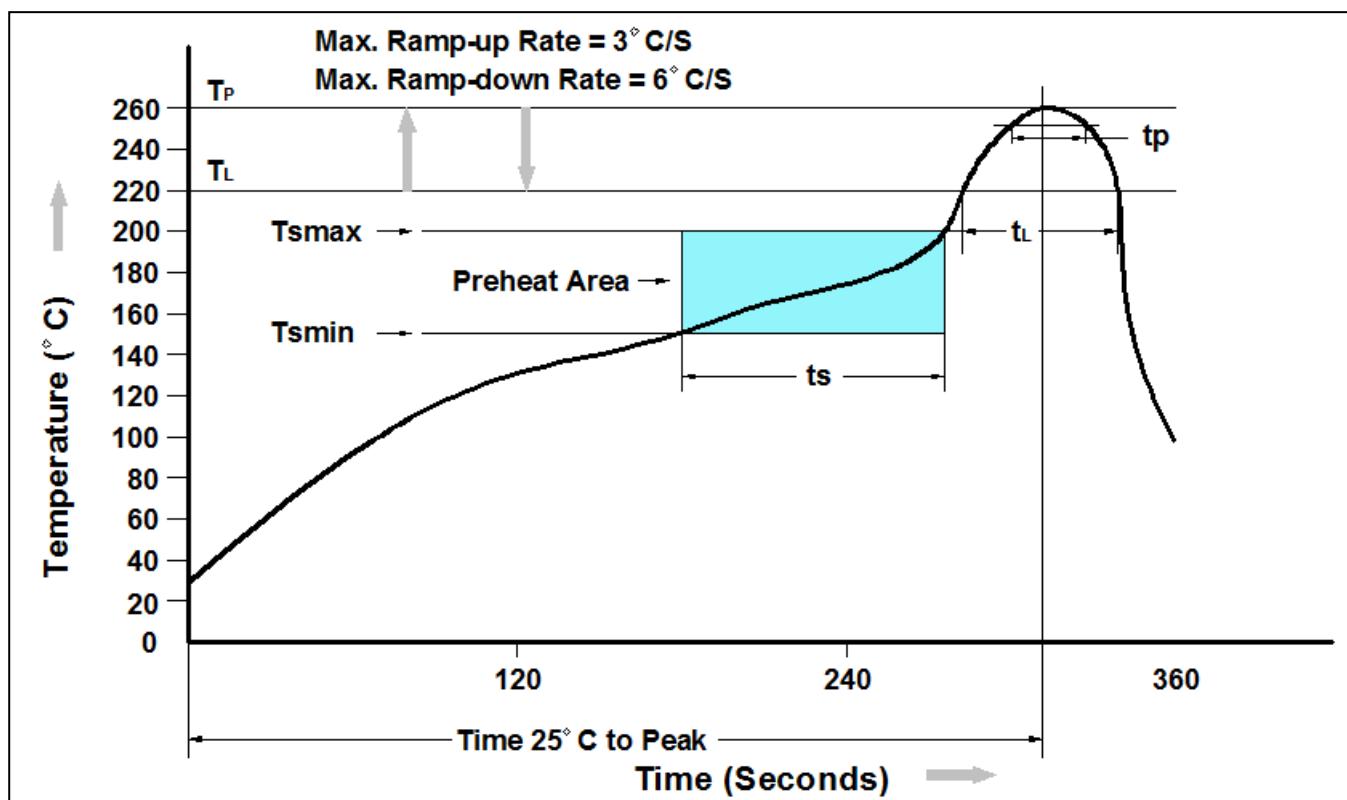


Fig.6 Normalized $R_{DS(on)}$ vs. T_J


Fig.7 Capacitance

Fig.8 Safe Operating Area

Fig.9 Normalized Maximum Transient Thermal Impedance

Fig.10 Switching Time Waveform

Fig.11 Gate Charge Waveform

➤ Recommand IR Reflow Soldering Thermal Profile

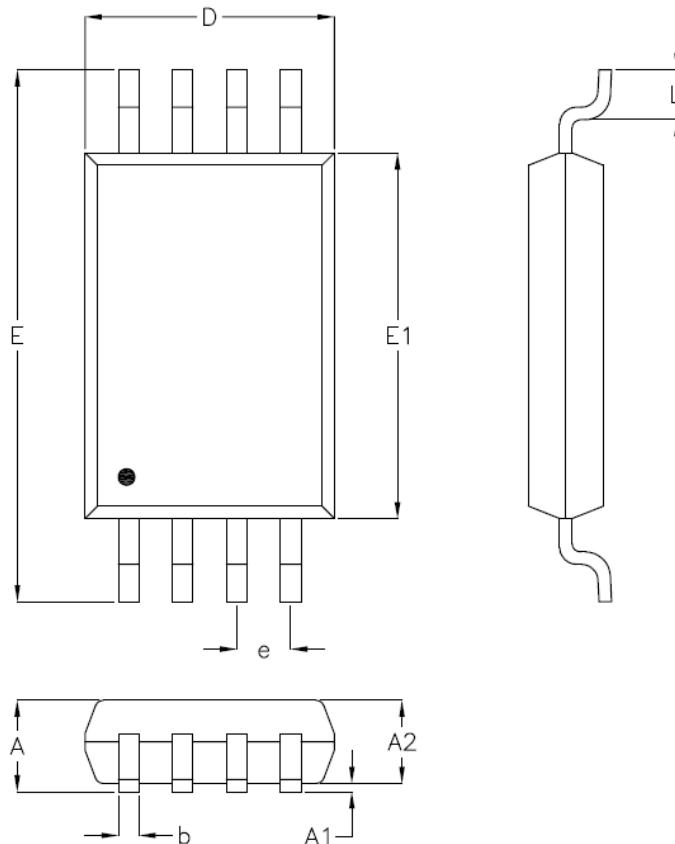


Profile Feature	Pb-Free Assembly Profile
Temperature Min. (T _{smin})	150°C
Temperature Max. (T _{smax})	200°C
Time (t _s) from (T _{smin} to T _{smax})	60-120 seconds
Average Ramp-up Rate (t _L to t _P)	3°C/second max.
Liquidous Temperature (T _L)	217°C
Time (t _L) Maintained Above (T _L)	60 – 150 seconds
Peak Temperature	260°C +0°C / -5°C
Time (t _P) within 5°C of actual Peak Temperature	30 seconds
Ramp-down Rate (T _P to T _L)	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.

➤ Ordering Information

Part Number	Description	Quantity
PAN27TE42JS	TSSOP-8 Reel	3000 pcs

➤ Package Information (TSSOP-8)



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	---	1.20	---	0.047
A1	0.05	0.15	0.002	0.006
A2	0.80	1.05	0.031	0.041
b	0.19	0.30	0.007	0.012
D	2.90	3.10	0.114	0.122
e	0.065 BSC		0.026 BSC	
E	6.25	6.55	0.246	0.258
E1	4.30	4.50	0.169	0.177
L	0.45	0.75	0.018	0.030

DISCLAIMER

- The information in this document and any product described herein are subject to change without notice and should not be construed as a commitment by Paceleader, Paceleader reserve the right to make changes to the information in this document.
- Though Paceleader make effort to improve product quality and reliability, Product can malfunction and fail due to their inherent electrical sensitivity and vulnerability to physical stress, it is the responsibility of the customer, when utilizing Paceleader products, to comply with the standards of safety in making a safe design for entire system and to avoid situation in which a malfunction or failure., In developing a new designs, customer should ensure that the device which shown in this documents are used within specified operating ranges.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by Paceleader for any infringements of patents or other rights of the third parties which may result from its use.