

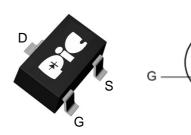
> General Description

This PAN2554NS N-Channel enhancement mode power field effect transistor is the high density trench technology and this advanced technology can provide excellent Rds(On) performance and efficiency for power switching and load switching application., this device also comply with the RoHS and Green Product requirement with full function reliability approved.

> Feature

- ■Super Low Gate Charge
- ●Green Device Available
- ●Excellent CdV/dt effect decline
- Advanced high cell density Trench technology
- ●SOT-23S package design

> **SOT-23S**



Application

- ●Load Switch
- Portable instrument
- ●MB / NB / 3C device

> Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _G s	\pm 12	V
Continuous Drain Current, V _{GS} @ 4.5V ¹	I _D @T _A =25°C	3.6	Α
Continuous Drain Current, V _{GS} @ 4.5V ¹	I _D @T _A =70°C	2.8	Α
Pulsed Drain Current ²	I _{DM}	14.4	Α
Total Power Dissipation ³	P _D @T _A =25°C	1	W
Storage Temperature Range	T _{STG}	-55 to 150	°C
Operating Junction Temperature Range	TJ	-55 to 150	$^{\circ}$ C
Thermal Resistance Junction-ambient ¹	R _{0JA}	125	°C/W
Thermal Resistance Junction-Case ¹	Rejc	80	°C/ W



Electrical Characteristics (T_J=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V , I _D =250uA	20			V
Static Drain-Source On-Resistance ²	D	V _{GS} =4.5V , I _D =3A			60	m ()
	R _{DS(ON)}	V_{GS} =2.5 V , I_D =2 A			80	mΩ
Gate Threshold Voltage	V _{GS(th)}	V _{GS} =V _{DS} , I _D =250uA	0.4		1.2	V
Drain-Source Leakage Current	la a a	V _{DS} =16V , V _{GS} =0V , T _J =25℃			1	uA
	IDSS	V_{DS} =16V , V_{GS} =0V , T_J =55 $^{\circ}$ C	, V _{GS} =0V , T _J =55℃		5	
Gate-Source Leakage Current	Igss	V_{GS} = \pm 12 V , V_{DS} =0 V			±100	nA
Forward Transconductance	gfs	V _{DS} =5V , I _D =3A		10.5		S
Total Gate Charge (4.5V)	Qg			4.6		
Gate-Source Charge	Q _{gs}	V _{DS} =15V , V _{GS} =4.5V , I _D =3A		0.7		nC
Gate-Drain Charge	Q_{gd}			1.5		
Turn-On Delay Time	T _{d(on)}			1.6		
Rise Time	Tr	V_{DD} =10V , V_{GS} =4.5V , R_{G} =3.3 Ω		42		
Turn-Off Delay Time	T _{d(off)}	I _D =3A		14		ns
Fall Time	T _f			7		
Input Capacitance	Ciss			310		
Output Capacitance	Coss	V _{DS} =15V , V _{GS} =0V , f=1MHz		49		pF
Reverse Transfer Capacitance	Crss			35		

Diode Characteristics

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous Source Current ^{1,4}	Is	V _G =V _D =0V , Force Current			3.6	Α
Diode Forward Voltage ²	V_{SD}	V _{GS} =0V , I _S =1A , T _J =25℃			1.2	٧

Note:

^{1.} Pulse width limited by maximum junction temperature.

^{2.}The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%

^{3.}Ensure that the channel temperature does not exceed 150°C.

^{4.}The data is theoretically the same as ID and IDM, in real applications, should be limited by total power dissipation.



> Typical Characteristics

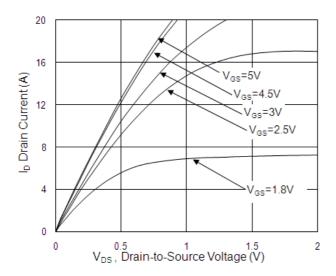


Fig.1 Typical Output Characteristics

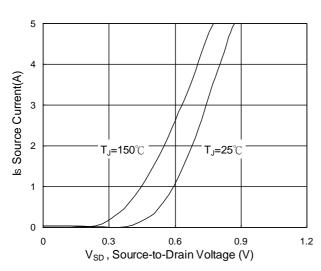


Fig.3 Forward Characteristics of Reverse

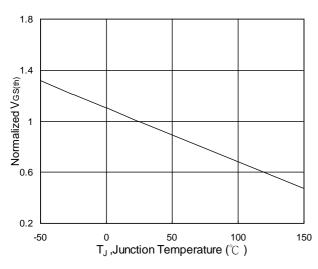


Fig.5 Normalized V_{GS(th)} vs. T_J

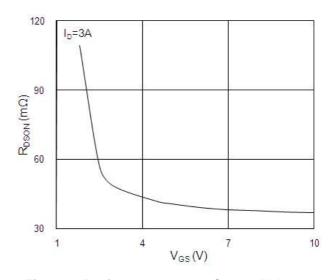


Fig.2 On-Resistance vs. Gate-Source Voltage

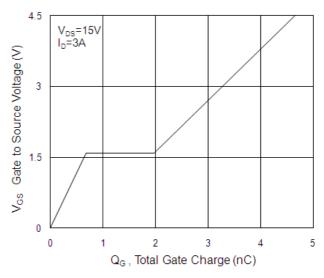


Fig.4 Gate-Charge Characteristics

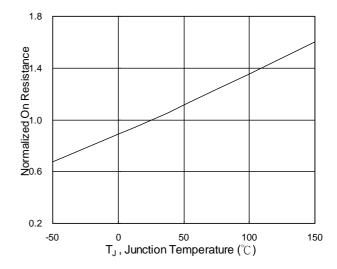
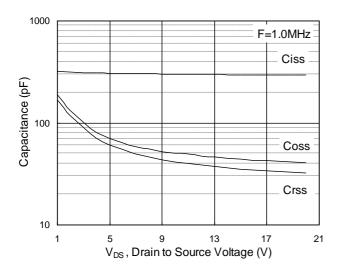


Fig.6 Normalized R_{DSON} vs. T_J





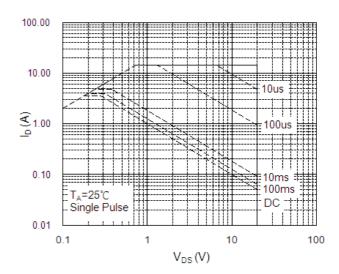


Fig.7 Capacitance

Fig.8 Safe Operating Area

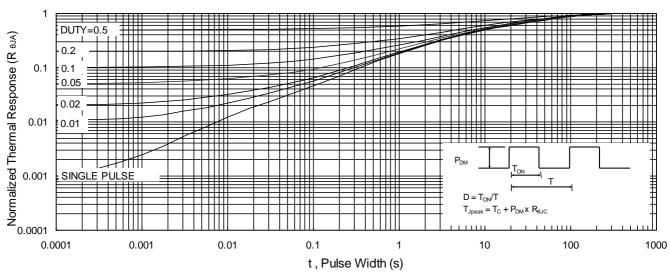
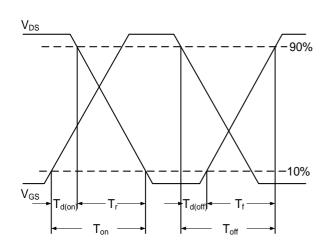


Fig.9 Normalized Maximum Transient Thermal Impedance





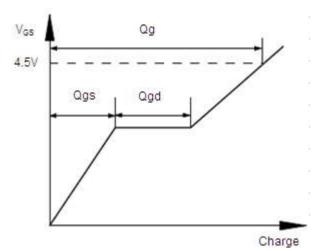
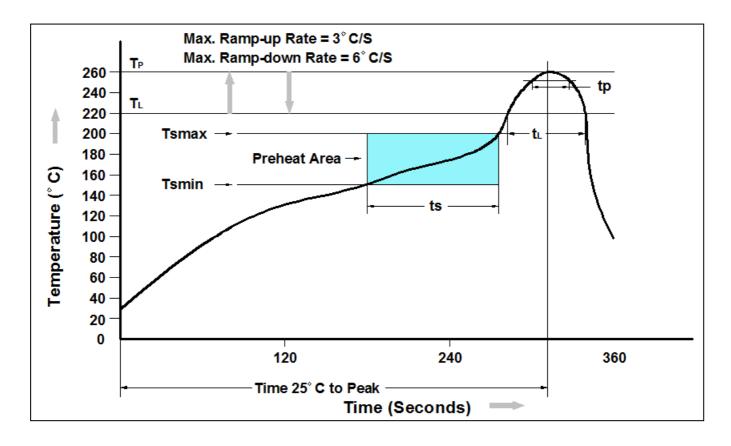


Fig.11 Gate Charge Waveform



Recommand IR Reflow Soldering Thermal Profile



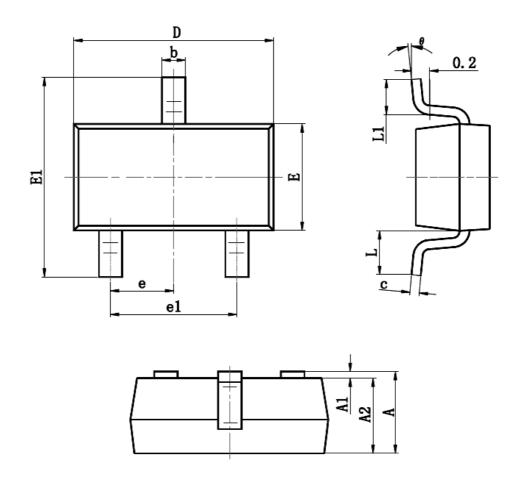
Profile Feature	Pb-Free Assembly Profile		
Temperature Min. (Tsmin)	150°C		
Temperature Max. (Tsmax)	200°C		
Time (ts) from (Tsmin to Tsmax)	60-120 seconds		
Average Ramp-up Rate (tL to tP)	3°C/second max.		
Liquidous Temperature (TL)	217°C		
Time (tL) Maintained Above (TL)	60 – 150 seconds		
Peak Temperature	260°C +0°C / -5°C		
Time (tP) within 5°C of actual Peak Temperature	30 seconds		
Ramp-down Rate (TP to TL)	6°C/second max		
Time 25°C to Peak Temperature	8 minutes max.		

Ordering Information

Part Number	Description	Quantity
PAN2554NS	SOT-23S Reel	3000 pcs



Package Information (SOT-23S)



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	0.900	1.200	0.035	0.043	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.100	0.035	0.039	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037	7 TYP	
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022	REF	
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	6°	



DISCLAIMER

- The information in this document and any product described herein are subject to change without notice and should not be construed as a commitment by Paceleader, Paceleader reserve the right to make changes to the information in this document.
- Though Paceleader make effort to improve product quality and reliability, Product can malfunction and fail due to their inherent electrical sensitivity and vulnerability to physical stress, it is the responsibility of the customer, when utilizing Paceleader products, to comply with the standards of safety in making a safe design for entire system and to avoid situation in which a malfunction or failure., In developing a new designs, customer should ensure that the device which shown in this documents are used within specified operating ranges.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by Paceleader for any infringements of patents or other rights of the third parties which may result from its use.