

#### > General Description

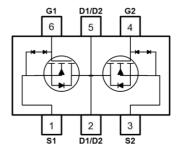
This PAN20E70C Dual N-Channel enhancement mode power field effect transistor is the high density trench technology and this advanced technology can provide excellent Rds(On) performance and efficiency for power switching and load switching application., this device also comply with the RoHS and Green Product requirement with full function reliability approved.

#### > Feature

- ■Super Low Gate Charge
- •Green Device Available
- ●Excellent CdV/dt effect decline
- ●ESD Protected
- Advanced high cell density Trench technology
- ●TSOP-6 package design







#### Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V <sub>DS</sub>	20	V
Gate-Source Voltage	V <sub>GS</sub>	±12	V
Continuous Drain Current <sup>1</sup>	I <sub>D</sub> @T <sub>A</sub> =25℃	7.0	А
Continuous Drain Current <sup>1</sup>	I <sub>D</sub> @T <sub>A</sub> =70°C	5.6	А
Pulsed Drain Current <sup>2</sup>	I <sub>DM</sub>	45	Α
Total Power Dissipation <sup>3</sup>	P <sub>D</sub> @T <sub>A</sub> =25℃	1.25	W
Storage Temperature Range	T <sub>STG</sub>	-55 to 150	$^{\circ}$
Operating Junction Temperature Range	TJ	-55 to 150	°C
Thermal Resistance Junction-ambient <sup>1</sup>	R <sub>θJA</sub>	100	°C/W



### Electrical Characteristics (T<sub>J</sub>=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	20			V	
Static Drain-Source On-Resistance <sup>2</sup>		$V_{GS}$ =4.5V , $I_D$ =3.5A	12	14	17	mΩ	
		V <sub>GS</sub> =4.0V , I <sub>D</sub> =3.5A	12.5	14.5	17.5		
	R <sub>DS(ON)</sub>	V <sub>GS</sub> =3.7V , I <sub>D</sub> =3.5A	13	15	18		
		V <sub>GS</sub> =3.1V , I <sub>D</sub> =3.5A	14	16	20		
		V <sub>GS</sub> =2.5V , I <sub>D</sub> =3.5A	16	19.5	24.5	1	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	0.5	0.7	1.2	V	
Drain-Source Leakage Current	l	V <sub>DS</sub> =16V , V <sub>GS</sub> =0V , T <sub>J</sub> =25℃			1	uA	
	IDSS	V <sub>DS</sub> =16V , V <sub>GS</sub> =0V , T <sub>J</sub> =55 °C			5		
Gate-Source Leakage Current	Igss	$V_{GS}=\pm8V$ , $V_{DS}$ = $0V$			±10	uA	
Forward Transconductance	gfs	V <sub>DS</sub> =5V , I <sub>D</sub> =3.5A		20		S	
Total Gate Charge	Qg			11			
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =15V , $V_{GS}$ =4.5V , $I_{D}$ =7A		1.2		nC	
Gate-Drain Charge	$Q_{gd}$			3.2			
Turn-On Delay Time	T <sub>d(on)</sub>			52			
Rise Time	Tr	$V_{DD}$ =15V , $V_{GS}$ =4.5V , $R_{G}$ =3.3 $\Omega$		302			
Turn-Off Delay Time	T <sub>d(off)</sub>	I <sub>D</sub> =3.5A		805		ns	
Fall Time	Tf			662			
Input Capacitance	Ciss			523			
Output Capacitance	Coss	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		81		pF	
Reverse Transfer Capacitance	Crss			56			

### Diode Characteristics

Parameter	Symbol	mbol Conditions		Тур.	Max.	Unit
Continuous Source Current <sup>1,4</sup>	ls	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			7	Α
Diode Forward Voltage <sup>2</sup>	$V_{SD}$	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25℃		0.78	1.2	V

#### Note:

Revision:D

<sup>1.</sup> Pulse width limited by maximum junction temperature.

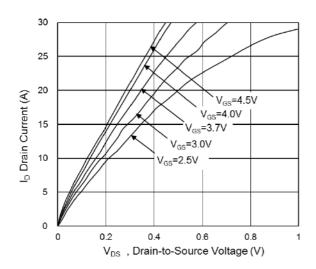
<sup>2.</sup>The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%

<sup>3.</sup>Ensure that the channel temperature does not exceed 150°C.

 $<sup>4.</sup> The \ data \ is \ theoretically \ the \ same \ as \ ID \ and \ IDM \ , \ in \ real \ applications \ , \ should \ be \ limited \ by \ total \ power \ dissipation.$ 



### Typical Characteristics



**Fig.1 Typical Output Characteristics** 

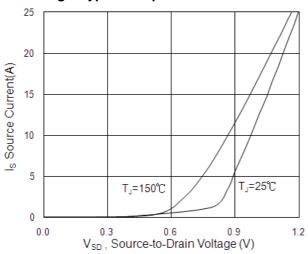


Fig.3 Source-Drain Forward Characteristics

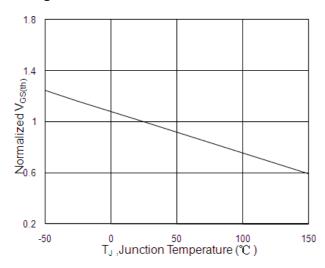


Fig.5 Normalized  $V_{\text{GS(th)}}$  vs.  $T_{\text{J}}$ 

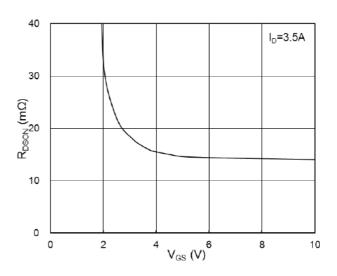


Fig.2 On-Resistance vs. G-S voltage

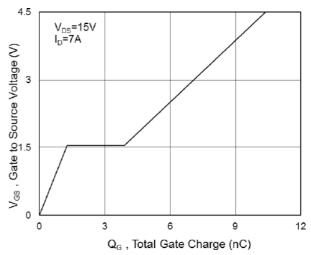


Fig.4 Gate-Charge Characteristics

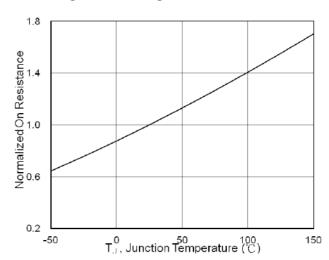
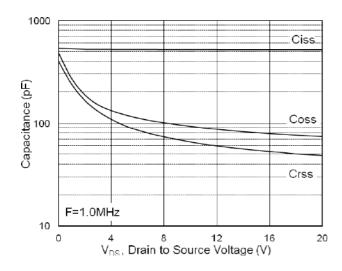


Fig.6 Normalized R<sub>DSON</sub> vs. T<sub>J</sub>

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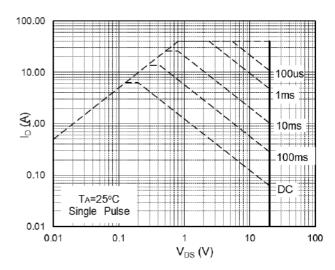


Fig.7 Capacitance

Fig.8 Safe Operating Area

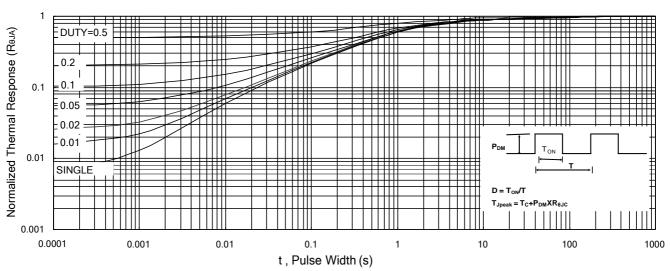
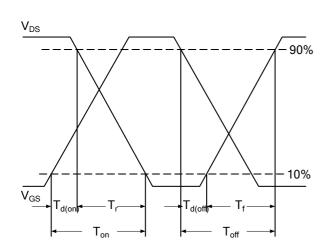


Fig.9 Normalized Maximum Transient Thermal Impedance





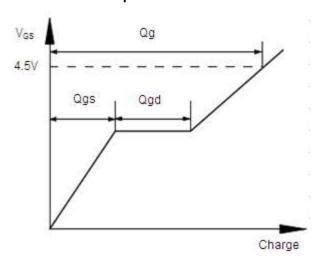
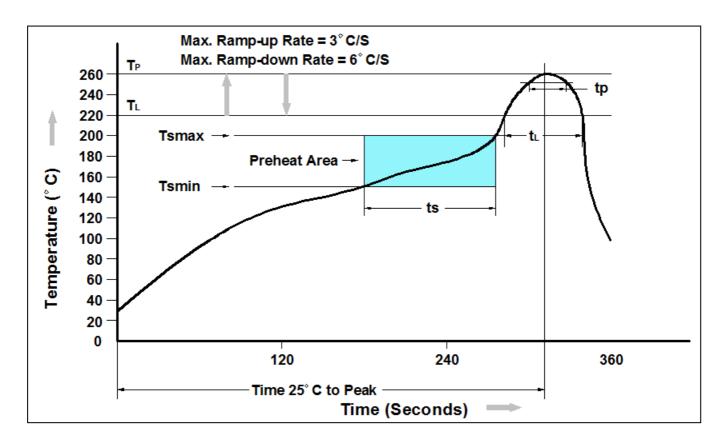


Fig.11 Gate Charge Waveform



### Recommand IR Reflow Soldering Thermal Profile



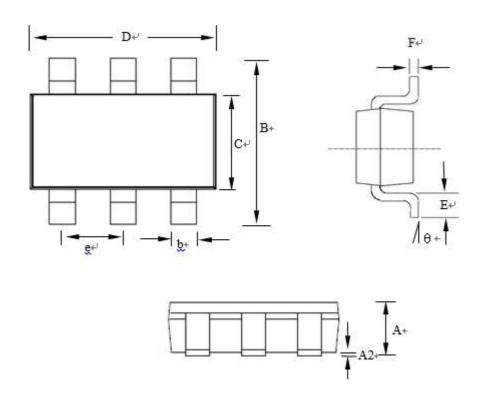
Profile Feature	Pb-Free Assembly Profile		
Temperature Min. (Tsmin)	150°C		
Temperature Max. (Tsmax)	200°C		
Time (ts) from (Tsmin to Tsmax)	60-120 seconds		
Average Ramp-up Rate (tL to tP)	3°C/second max.		
Liquidous Temperature (TL)	217°C		
Time (tL) Maintained Above (TL)	60 – 150 seconds		
Peak Temperature	260°C +0°C /-5°C		
Time (tP) within 5°C of actual Peak Temperature	30 seconds		
Ramp-down Rate (TP to TL)	6°C/second max		
Time 25°C to Peak Temperature	8 minutes max.		

## Ordering Information

Part Number	Description	Quantity
PAN20E70C	TSOP-6 Reel	3000 pcs



### Package Information (TSOP-6)



SYMBOLS	MILLIMETERS			INCHES		
STWIDOLS	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.70		0.9	0.028		0.035
A2	0.00	i	0.10	0.000		0.004
В	2.60	2.80	3.00	0.102	0.110	0.118
С	1.40	1.60	1.80	0.055	0.063	0.071
D	2.70	2.90	3.10	0.106	0.114	0.122
Е	0.30	0.40	0.60	0.012	0.016	0.024
F	0.07	0.127	0.20	0.003	0.005	0.008
b	0.30	0.40	0.50	0.012	0.016	0.020
е		0.95			0.037	
θ	0°	5°	10°	0°	5°	10°





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