

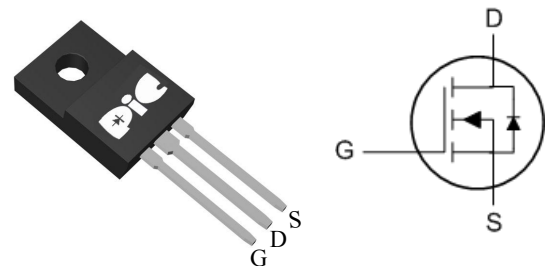
### ➤ General Description

This PAN00TF18GF N-Channel enhancement mode power field effect transistor is the high density trench technology and this advanced technology can provide excellent  $R_{ds(On)}$  performance and efficiency for power switching and load switching application., this device also comply with the RoHS and Green Product requirement with full function reliability approved.

### ➤ Feature

- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available
- Excellent  $C_{dv}/dt$  effect decline
- Advanced high cell density Trenchtechnology

### ➤ TO220F



### ➤ Application

- SMPS Power Supplier
- Charger Adapter
- Power Tools
- LED Lighting

### ➤ Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D @ T_C=25^\circ C$	27	A
Continuous Drain Current	$I_D @ T_C=100^\circ C$	19	A
Continuous Drain Current	$I_D @ T_A=25^\circ C$	6	A
Continuous Drain Current	$I_D @ T_A=70^\circ C$	5	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	120	A
Single Pulse Avalanche Energy <sup>3</sup>	EAS	48	mJ
Avalanche Current	$I_{AS}$	31	A
Total Power Dissipation <sup>4</sup>	$P_D @ T_C=25^\circ C$	42	W
Total Power Dissipation <sup>4</sup>	$P_D @ T_A=70^\circ C$	1.5	W
Storage Temperature Range	$T_{STG}$	-55 to 175	$^\circ C$
Operating Junction Temperature Range	$T_J$	-55 to 175	$^\circ C$
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	58	$^\circ C/W$
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	3.5	$^\circ C/W$

### ➤ Electrical Characteristics ( $T_J=25^\circ C$ Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V$ , $I_D=250\mu A$	100	---	---	V
Static Drain-Source On-Resistance <sup>2</sup>	$R_{DS(ON)}$	$V_{GS}=10V$ , $I_D=20A$	---	18	22	$m\Omega$
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}$ , $I_D=250\mu A$	2.5	---	4.5	V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=100V$ , $V_{GS}=0V$ , $T_J=25^\circ C$	---	---	10	$\mu A$
		$V_{DS}=100V$ , $V_{GS}=0V$ , $T_J=55^\circ C$	---	---	50	
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V$ , $V_{DS}=0V$	---	---	$\pm 100$	nA
Forward Transconductance	$g_{fs}$	$V_{DS}=5V$ , $I_D=20A$	---	33	---	S
Total Gate Charge (10V)	$Q_g$	$V_{DS}=80V$ , $V_{GS}=10V$ , $I_D=20A$	---	27.6	---	nC
Gate-Source Charge	$Q_{gs}$		---	1.4	---	
Gate-Drain Charge	$Q_{gd}$		---	7.9	---	
Turn-On Delay Time	$T_{d(on)}$	$V_{DD}=50V$ , $V_{GS}=10V$ , $R_G=3.3\Omega$ , $I_D=20A$	---	6.5	---	ns
Rise Time	$T_r$		---	35	---	
Turn-Off Delay Time	$T_{d(off)}$		---	7.5	---	
Fall Time	$T_f$		---	12	---	
Input Capacitance	$C_{iss}$	$V_{DS}=15V$ , $V_{GS}=0V$ , $f=1MHz$	---	1890	---	$\mu F$
Output Capacitance	$C_{oss}$		---	268	---	
Reverse Transfer Capacitance	$C_{rss}$		---	67	---	

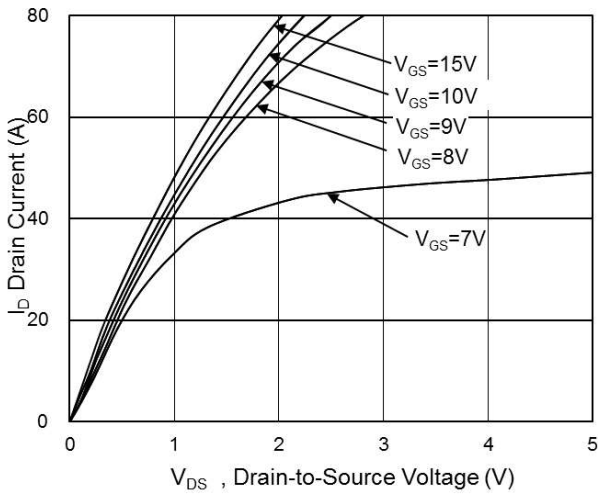
### ➤ Diode Characteristics

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Continuous Source Current <sup>1,5</sup>	$I_S$	$V_G=V_D=0V$ , Force Current	---	---	40	A
Diode Forward Voltage <sup>2</sup>	$V_{SD}$	$V_{GS}=0V$ , $I_S=1A$ , $T_J=25^\circ C$	---	---	1.2	V
Reverse Recovery Time	$t_{rr}$	$I_F=20A$ , $di/dt=100A/\mu s$ ,	---	22	---	nS
Reverse Recovery Charge	$Q_{rr}$	$T_J=25^\circ C$	---	20	---	

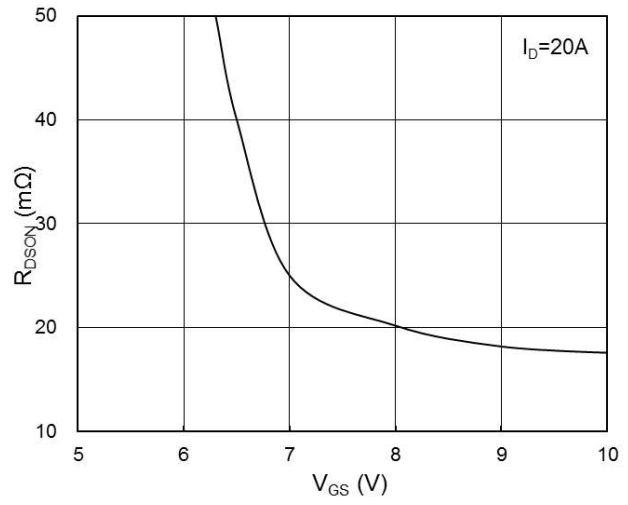
Note :

1. Pulse width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$
3. The EAS data shows Max. rating. The test condition is  $V_{DS}=25V$ ,  $V_{GS}=10V$ ,  $L=0.1mH$ ,  $I_{AS}=31A$
4. Ensure that the channel temperature does not exceed  $150^\circ C$ .
5. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.

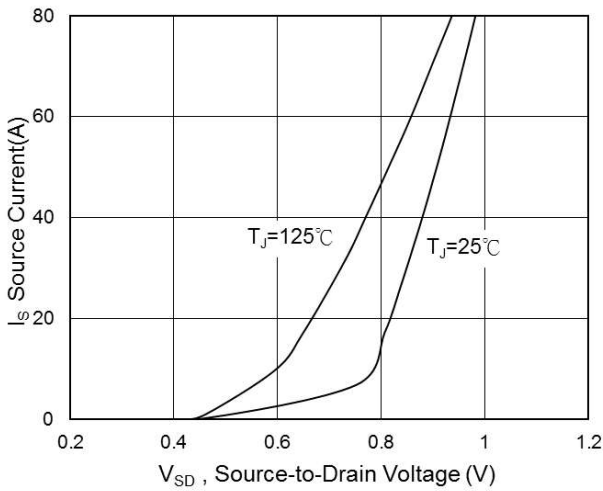
### ➤ Typical Characteristics



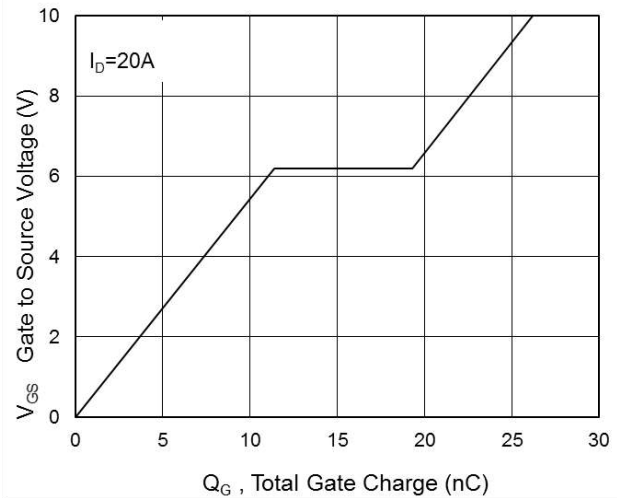
**Fig.1 Typical Output Characteristics**



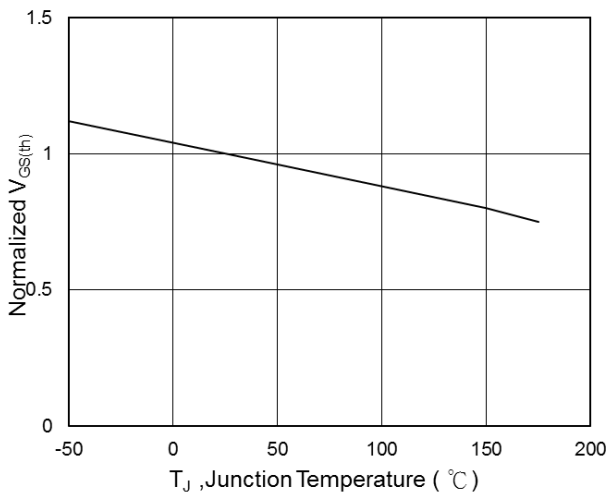
**Fig.2 On-Resistance vs. G-S Voltage**



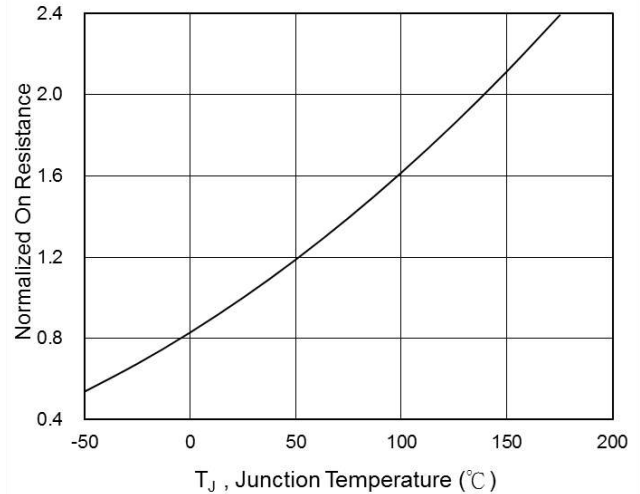
**Fig.3 Source Drain Forward Characteristics**



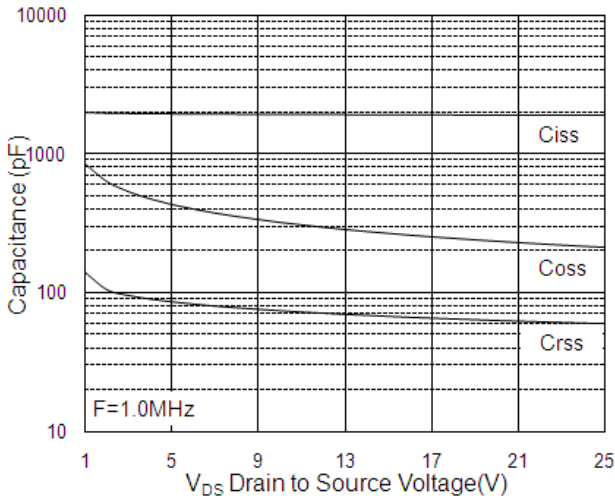
**Fig.4 Gate-Charge Characteristics**



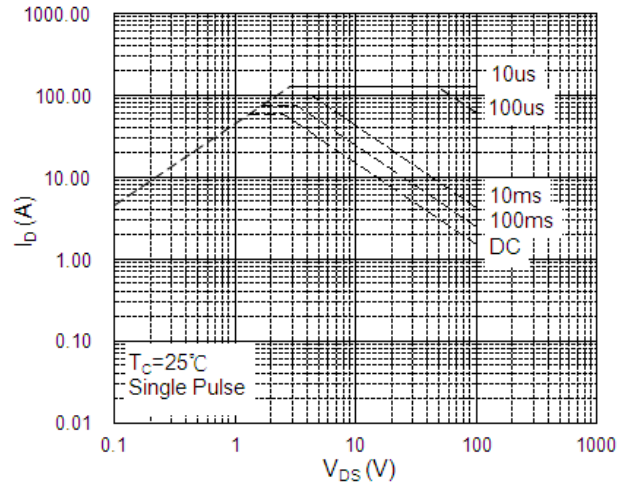
**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**



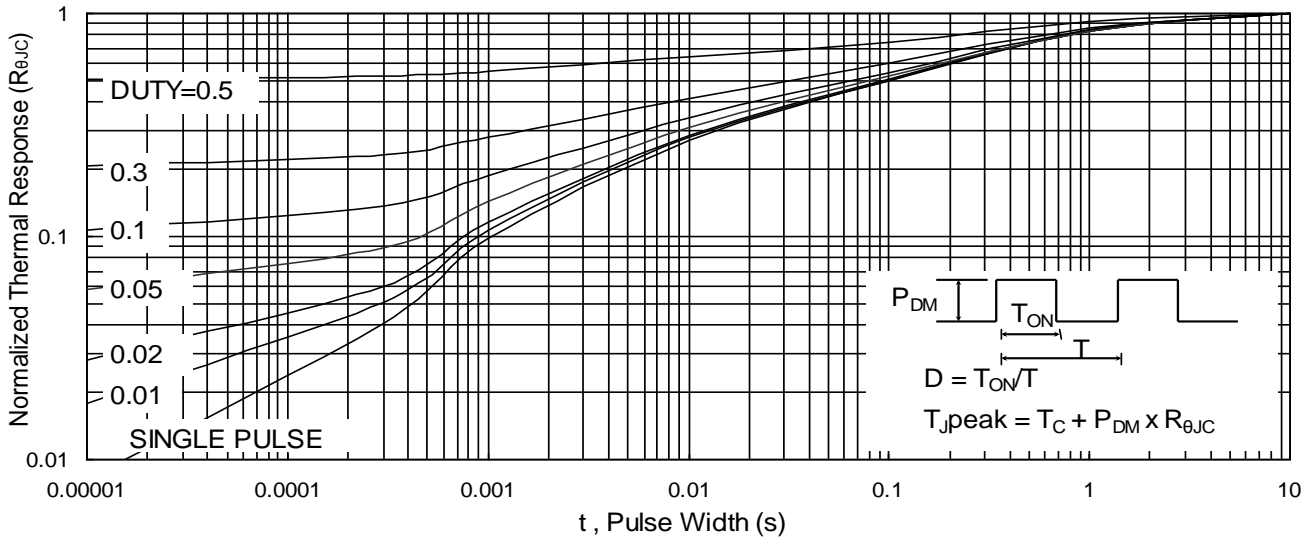
**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**



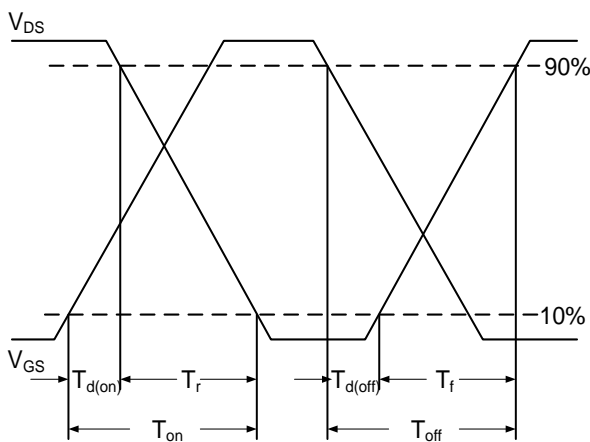
**Fig.7 Capacitance**



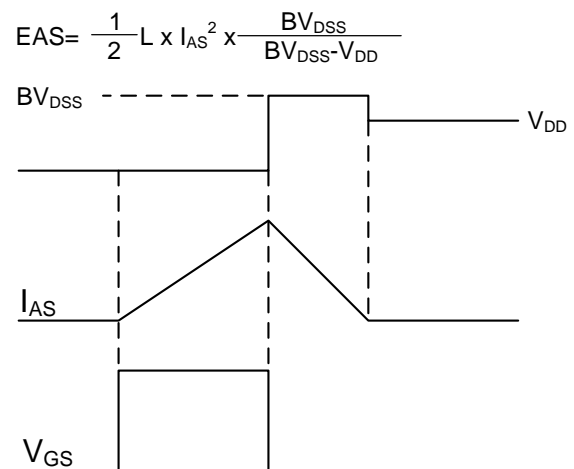
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**

### ➤ Recommand IR Reflow Soldering Thermal Profile

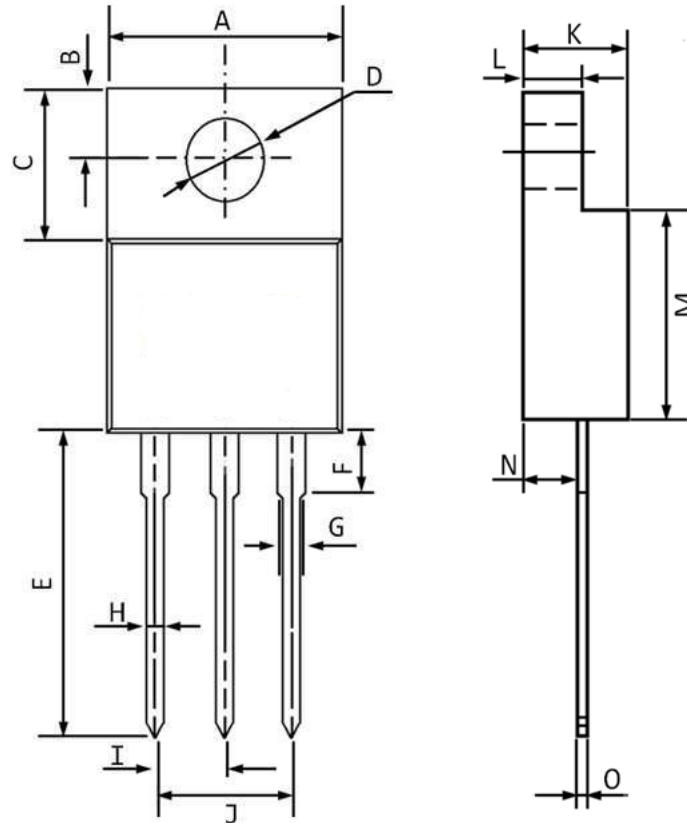


Profile Feature	Pb-Free Assembly Profile
Temperature Min. (T <sub>smin</sub> )	150°C
Temperature Max. (T <sub>smax</sub> )	200°C
Time (t <sub>s</sub> ) from (T <sub>smin</sub> to T <sub>smax</sub> )	60-120 seconds
Average Ramp-up Rate (t <sub>L</sub> to t <sub>P</sub> )	3°C/second max.
Liquidous Temperature (T <sub>L</sub> )	217°C
Time (t <sub>L</sub> ) Maintained Above (T <sub>L</sub> )	60 – 150 seconds
Peak Temperature	260°C +0°C / -5°C
Time (t <sub>P</sub> ) within 5°C of actual Peak Temperature	30 seconds
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.

### ➤ Ordering Information

Part Number	Description	Quantity
PAN00TF18GF	TO-220F / 50 pcs/tube	1000 pcs

### ➤ Package Information ( TO-220F )



SYMBOLS	MILLIMETERS		INCHES	
	Min.	Max.	Min.	Max.
A	—	10.50	—	0.414
B	2.60	3.00	0.102	0.118
C	6.70	7.10	0.264	0.280
D	2.90	3.50	0.114	0.138
E	13.10	13.90	0.516	0.548
F	—	4.00	—	0.158
G	1.11	1.45	0.044	0.057
H	0.40	0.80	0.016	0.032
I	2.40	2.80	0.095	0.110
J	5.00	5.40	0.197	0.213
K	4.30	4.70	0.169	0.185
L	2.90	3.30	0.114	0.130
M	8.20	9.00	0.323	0.355
N	2.50	2.90	0.099	0.114
O	0.40	0.80	0.016	0.032

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