

➤ General Description

The PAE3331W is designed with latest process TVS technology to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space comes at a premium. Also because of its low capacitance, it is suited for use in high frequency designs such as USB 2.0 high speed, USB 3.0 super speed, USB 3.1 super speed ,VGA, DVI, HDMI, eSATA and other high speed line applications.

It has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by ESD(electrostatic discharge), and EFT (electrical fast transients).

➤ Feature

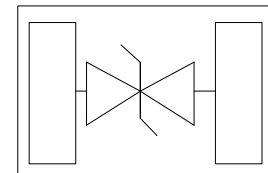
- Peak Power Dissipation – 40 W (8 x 20 us Waveform)
- Stand-off Voltage: 3.3 V
- Low capacitance (<0.25pF) for high-speed interfaces
- No insertion loss to 10.0GHz
- Protects I/O Port
- Low Clamping Voltage
- Low Leakage
- Low Capacitance
- Meets MSL 1 Requirements
- ROHS compliant
- **Solid-state Punch-Through TVS Process technology**

➤ DFN0603-2



➤ Application

- High Speed Line :USB1.0/2.0/3.0/3.1,VGA,DVI,SDI,
- High Definition Multi-Media Interface (HDMI1.3/1.4/2.0)
- Serial and Parallel Ports
- Notebooks, Desktops, Servers
- Projection TV
- Cellular handsets and accessories
- Portable instrumentation
- Peripherals



➤ Protection solution to meet

- IEC61000-4-2 (ESD) ±20kV (air), ±20kV (contact)

➤ Maximum Ratings (TA=25°C Unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (tp=8/20µs waveform)	P _{PPP}	40	Watts
ESD Rating per IEC61000-4-2:	Contact	20	KV
	Air	20	
Lead Soldering Temperature	T _L	260 (10 sec.)	°C
Operating Temperature Range	T _J	-55 ~ 150	°C
Storage Temperature Range	T _{STG}	-55 ~ 150	°C
Lead Solder Temperature – Maximum (10 Second Duration)	T _L	260	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

*Other voltages may be available upon request.

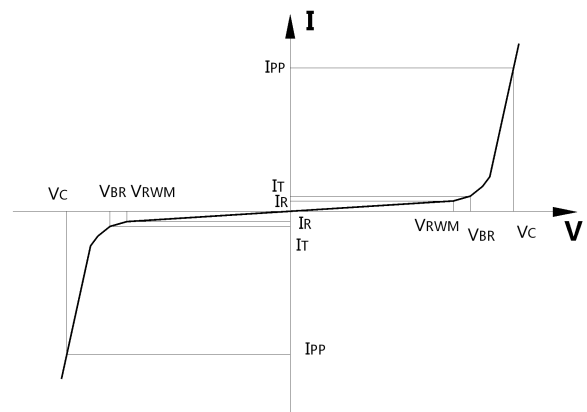
1. Non-repetitive current pulse, per Figure 1.

➤ Electrical Characteristics (TA=25°C Unless otherwise specified)

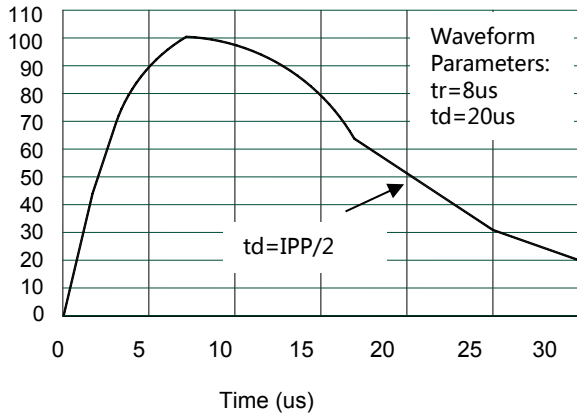
Device	V _{RWM}	I _R @ V _{RWM}		V _{BR} @ 1 mA	R _{dyn}	V _C	Capacitance	
		(uA)		(Volts)	Ω	@ 2 A	@ V _R = 0 V, 1 MHz (pF)	
	(V)	Typ.	Max	Min	Typ.	(V)	Typ	Max
PAE3331W	3.3	0.01	0.1	4.0	0.52	13	0.15	0.25

Junction capacitance is measured in VR=0V,F=1MHz

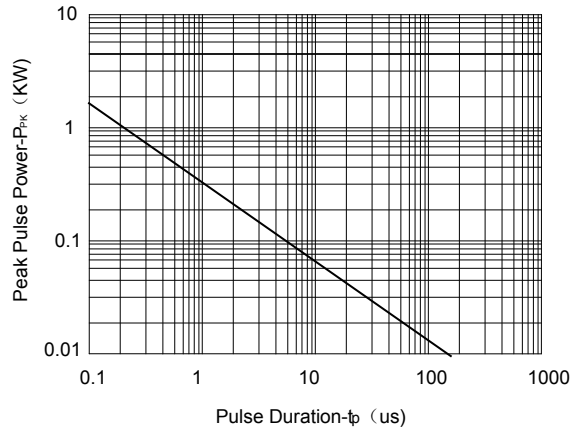
Symbol	Parameter
V _{RWM}	Working Peak Reverse Voltage
V _{BR}	Breakdown Voltage @ I _T
V _C	Clamping Voltage @ I _{PP}
I _T	Test Current
I _{RM}	Leakage current at V _{RWM}
I _{PP}	Peak pulse current
C _O	Off-state Capacitance
C _J	Junction Capacitance



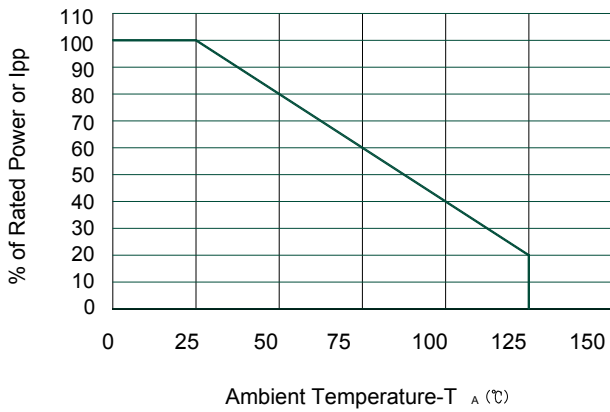
➤ Typical Characteristics



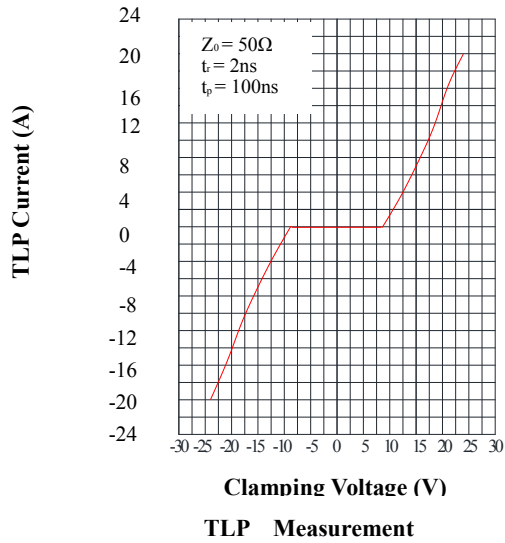
Pulse Waveform



Non-Repetitive Peak Pulse Power vs. Pulse Time



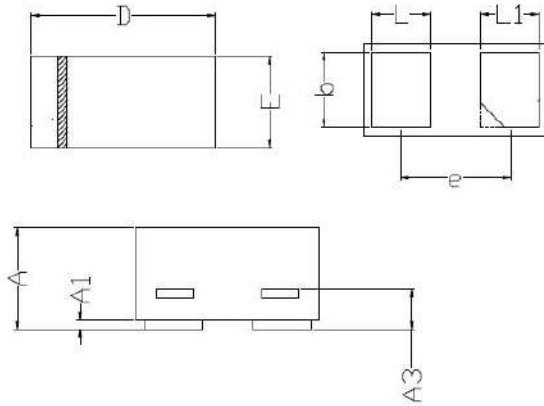
Power Derating Curve



TLP Measurement

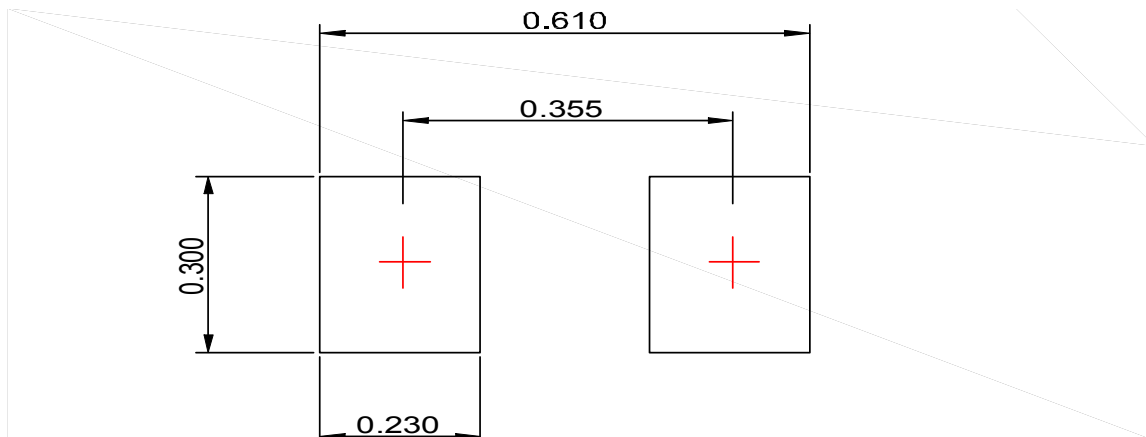
➤ Package Information (DFN0603-2)

Case Material: Molded Plastic. UL Flammability



DIM	Millimeters	
	Min	Max
A	0.230	0.330
A1	0.000	0.050
A3	0.102REF	
D	0.550	0.650
E	0.250	0.350
b	0.215	0.275
L	0.115	0.175
L1	0.115	0.175
e	0.40BSC	

Recommended Pad outline



➤ Ordering Information

Part Number	Description	Quantity
PAE3331W	DFN0603-2 Reel	15000 pcs

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