

N-Ch and P-Ch Fast Switching MOSFET
V_{DS}=60V, I_D=4.8A, R_{DSON}=32mΩ
V_{DS}=-60V, I_D=-3.7A, R_{DSON}=70mΩ

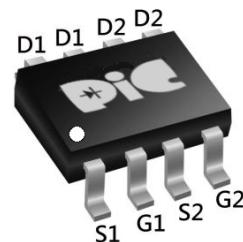
➤ General Description

This PAC69TJ01J N&P Channel enhancement mode power field effect transistor is the high density trench technology and this advanced technology can provide excellent Rds(On) performance and efficiency for power switching and load switching application., this device also comply with the RoHS and Green Product requirement with full function reliability approved.

➤ Feature

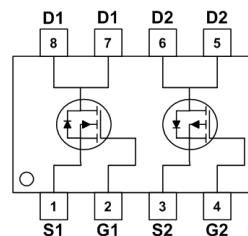
- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

➤ SOP-8



➤ Application

- Notebook CPU Core-High-Side Switch



➤ Absolute Maximum Ratings

Parameter	Symbol	Rating		Units
		N-Channel	P-Channel	
Drain-Source Voltage	V _{DS}	60	-60	V
Gate-Source Voltage	V _{GS}	±20	±20	V
Continuous Drain Current, V _{GS} @ 10V ¹	I _D @T _A =25°C	4.8	-3.7	A
Continuous Drain Current, V _{GS} @ 10V ¹	I _D @T _A =70°C	3.8	-3	A
Pulsed Drain Current ²	I _{DM}	9.6	-7.5	A
Single Pulse Avalanche Energy ³	EAS	25.5	35.3	mJ
Avalanche Current	I _{AS}	22.6	-26.6	A
Total Power Dissipation ⁴	P _D @T _A =25°C	1.5	1.5	W
Storage Temperature Range	T _{STG}	-55 to 150	-55 to 150	°C
Operating Junction Temperature Range	T _J	-55 to 150	-55 to 150	°C
Thermal Resistance Junction-Ambient ¹	R _{θJA}	---	85	°C/W
Thermal Resistance Junction-Case ¹	R _{θJC}	---	36	°C/W

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V_{DS}=-60V, I_D=-3.7A, RDS(ON)=70mΩ

➤ N-Channel Electrical Characteristics (T_J=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V , I _D =250uA	60	---	---	V
BV _{DSS} Temperature Coefficient	Δ BV _{DSS} /Δ T _J	Reference to 25°C , I _D =1mA	---	0.063	---	V/°C
Static Drain-Source On-Resistance ²	R _{DS(ON)}	V _{GS} =10V , I _D =4A	---	---	32	mΩ
		V _{GS} =4.5V , I _D =2A	---	---	38	
Gate Threshold Voltage	V _{GS(th)}	V _{GS} =V _{DS} , I _D =250uA	1.2	---	2.5	V
V _{GS(th)} Temperature Coefficient	Δ V _{GS(th)}		---	-5.24	---	mV/°C
Drain-Source Leakage Current	I _{DSS}	V _{DS} =48V , V _{GS} =0V , T _J =25°C	---	---	1	uA
		V _{DS} =48V , V _{GS} =0V , T _J =55°C	---	---	5	
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V , V _{DS} =0V	---	---	±100	nA
Forward Transconductance	g _f s	V _{DS} =5V , I _D =4A	---	21	---	S
Gate Resistance	R _g	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	3.2	---	Ω
Total Gate Charge (4.5V)	Q _g	V _{DS} =48V , V _{GS} =4.5V , I _D =4A	---	12.6	---	nC
Gate-Source Charge	Q _{gs}		---	3.2	---	
Gate-Drain Charge	Q _{gd}		---	6.3	---	
Turn-On Delay Time	T _{d(on)}	V _{DD} =30V , V _{GS} =10V , R _G =3.3Ω, I _D =4A	---	8	---	ns
Rise Time	T _r		---	14.2	---	
Turn-Off Delay Time	T _{d(off)}		---	24.4	---	
Fall Time	T _f		---	4.6	---	
Input Capacitance	C _{iss}	V _{DS} =15V , V _{GS} =0V , f=1MHz	---	1378	---	pF
Output Capacitance	C _{oss}		---	86	---	
Reverse Transfer Capacitance	C _{rss}		---	64	---	

➤ Diode Characteristics

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Continuous Source Current ^{1,5}	I _s	V _G =V _D =0V , Force Current	---	---	4.8	A
Pulsed Source Current ^{2,5}	I _{SM}		---	---	9.6	A
Diode Forward Voltage ²	V _{SD}	V _{GS} =0V , I _s =1A , T _J =25°C	---	---	1.2	V

Note :

1.Pulse width limited by maximum junction temperature.

2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%

3.The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=10V,L=0.1mH,I_{AS}=22.6A

4.Ensure that the channel temperature does not exceed 150°C.

5.The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

N-Ch and P-Ch Fast Switching MOSFET
V_{DS}=60V, I_D=4.8A, R_{DS(ON)}=32mΩ
V_{DS}=-60V, I_D=-3.7A, R_{DS(ON)}=70mΩ

➤ P-Channel Electrical Characteristics (T_J=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V , I _D =-250uA	-60	---	---	V
BV _{DSS} Temperature Coefficient	Δ BV _{DSS} /Δ T _J	Reference to 25°C , I _D =-1mA	---	-0.03	---	V/°C
Static Drain-Source On-Resistance ²	R _{DS(ON)}	V _{GS} =-10V , I _D =-3A	---	---	70	mΩ
		V _{GS} =-4.5V , I _D =-2A	---	---	105	
Gate Threshold Voltage	V _{GS(th)}	V _{GS} =V _{DS} , I _D =-250uA	-1.2	---	-2.5	V
V _{GS(th)} Temperature Coefficient	Δ V _{GS(th)}		---	4.56	---	mV/°C
Drain-Source Leakage Current	I _{DSS}	V _{DS} =-48V , V _{GS} =0V , T _J =25°C	---	---	1	uA
		V _{DS} =-48V , V _{GS} =0V , T _J =55°C	---	---	5	
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V , V _{DS} =0V	---	---	±100	nA
Forward Transconductance	g _f s	V _{DS} =-5V , I _D =-3A	---	15	---	S
Gate Resistance	R _g	V _{DS} =0V , V _{GS} =0V , f=1MHz	---	13.5	---	Ω
Total Gate Charge (-4.5V)	Q _g	V _{DS} =-48V , V _{GS} =-4.5V , I _D =-3A	---	9.86	---	nC
Gate-Source Charge	Q _{gs}		---	3.1	---	
Gate-Drain Charge	Q _{gd}		---	2.95	---	
Turn-On Delay Time	T _{d(on)}	V _{DD} =-15V , V _{GS} =-10V , R _G =3.3Ω, I _D =-1A	---	28.8	---	ns
Rise Time	T _r		---	19.8	---	
Turn-Off Delay Time	T _{d(off)}		---	60.8	---	
Fall Time	T _f		---	7.2	---	
Input Capacitance	C _{iss}	V _{DS} =-15V , V _{GS} =0V , f=1MHz	---	1447	---	pF
Output Capacitance	C _{oss}		---	97.3	---	
Reverse Transfer Capacitance	C _{rss}		---	70	---	

➤ Diode Characteristics

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Continuous Source Current ^{1,5}	I _S	V _G =V _D =0V , Force Current	---	---	-3.7	A
Pulsed Source Current ^{2,5}	I _{SM}		---	---	-7.5	A
Diode Forward Voltage ²	V _{SD}	V _{GS} =0V , I _S =-1A , T _J =25°C	---	---	-1.2	V

Note :

1.Pulse width limited by maximum junction temperature.

2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%

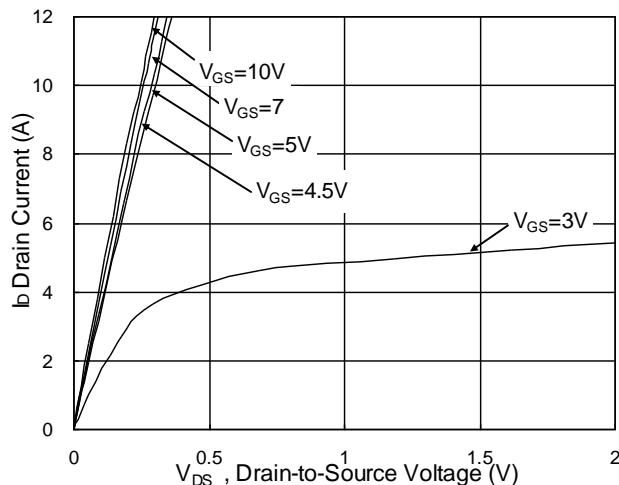
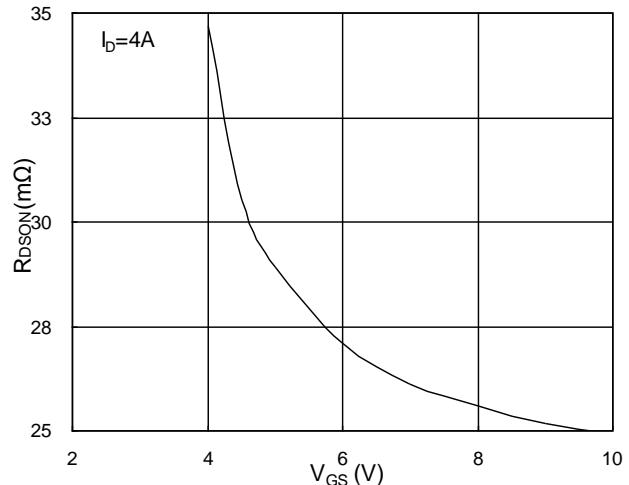
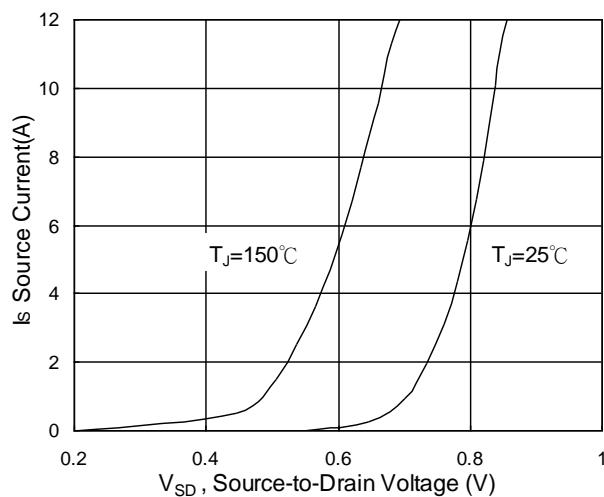
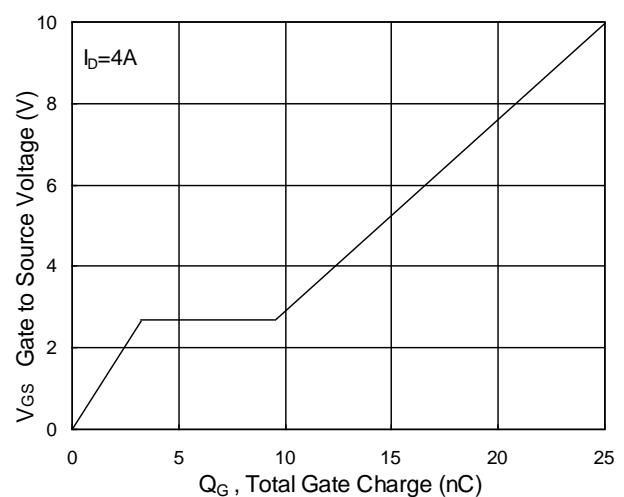
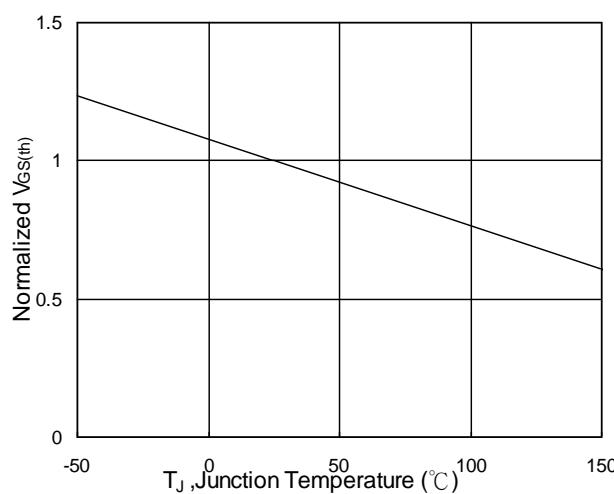
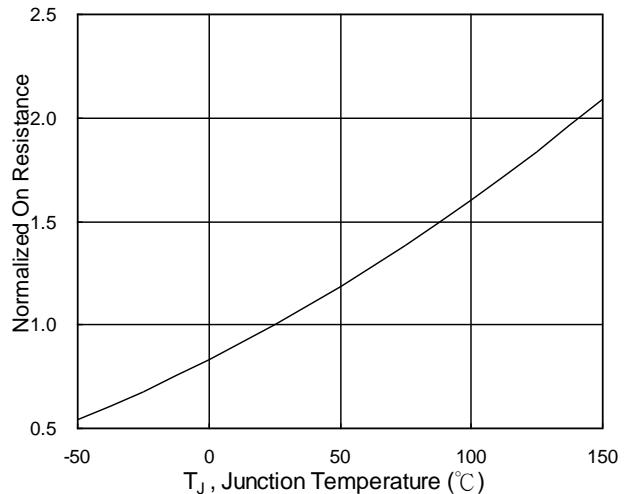
3.The EAS data shows Max. rating . The test condition is V_{DD}=-25V,V_{GS}=-10V,L=0.1mH,I_{AS}=-26.6A

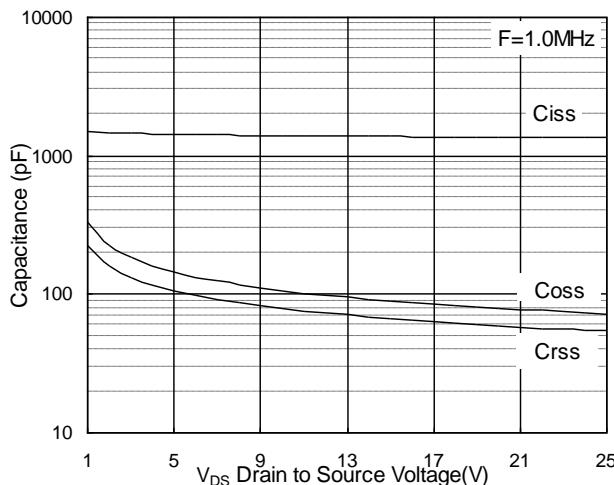
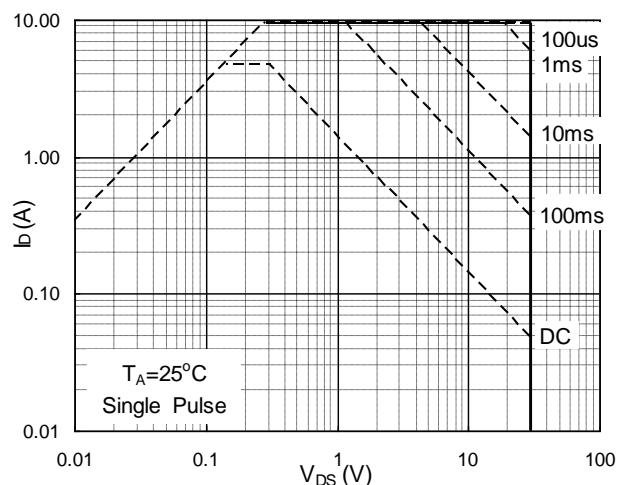
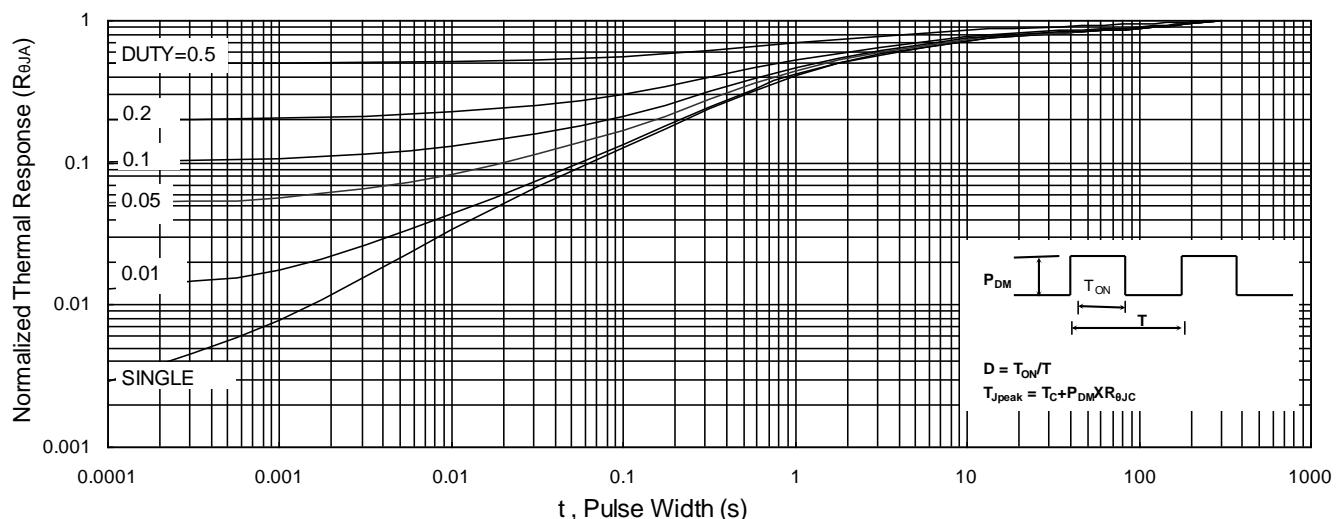
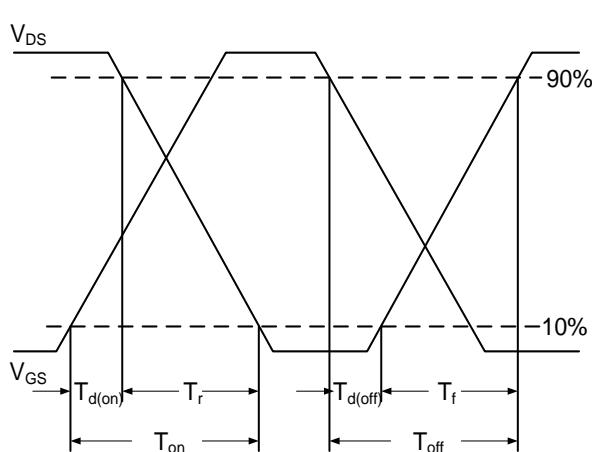
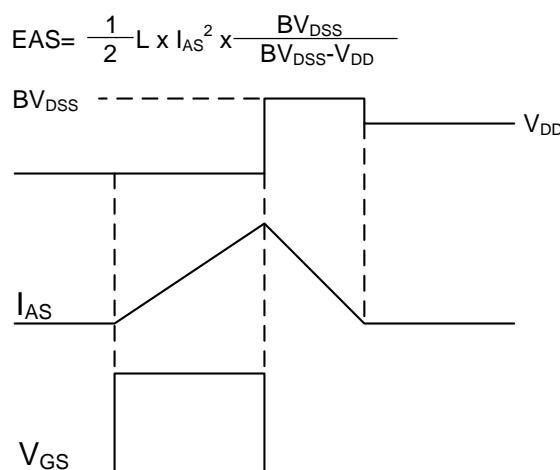
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 $V_{DS}=-60V$, $I_D=-3.7A$, $R_{DS(ON)}=70m\Omega$

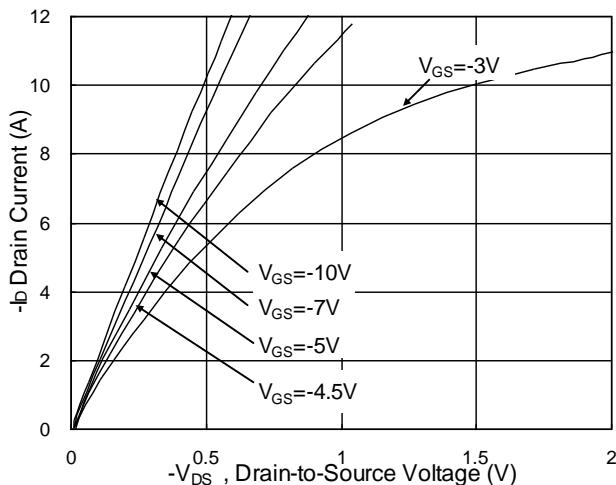
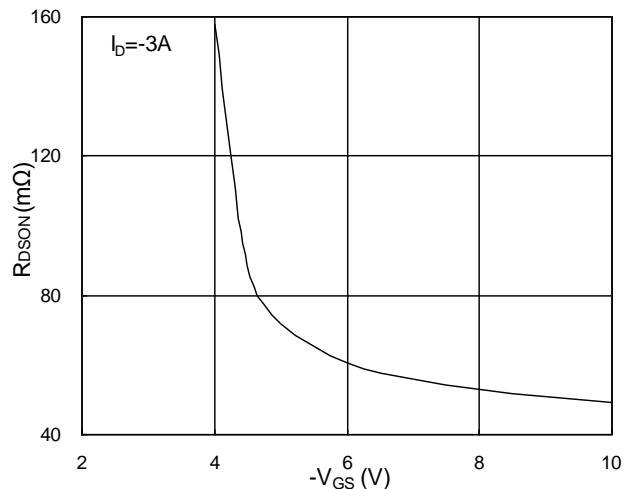
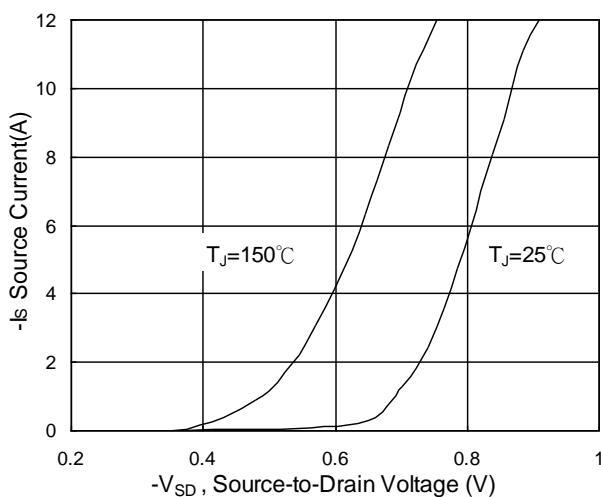
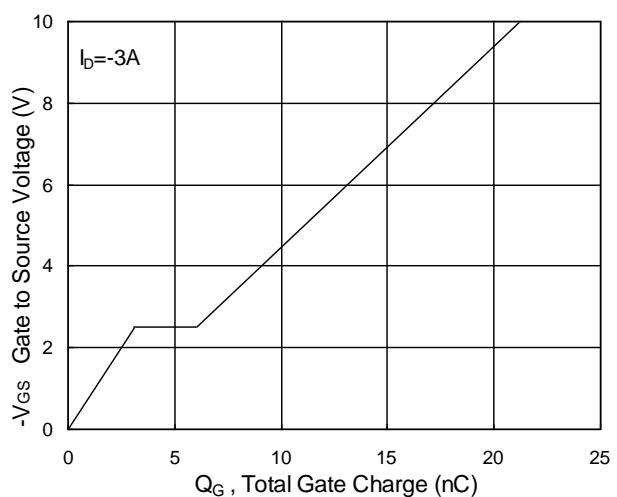
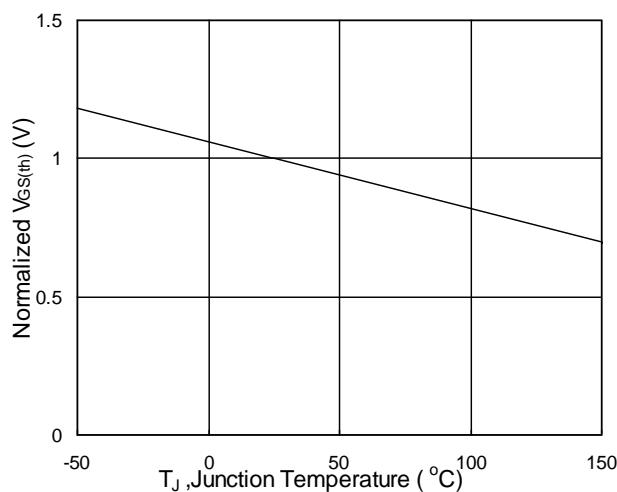
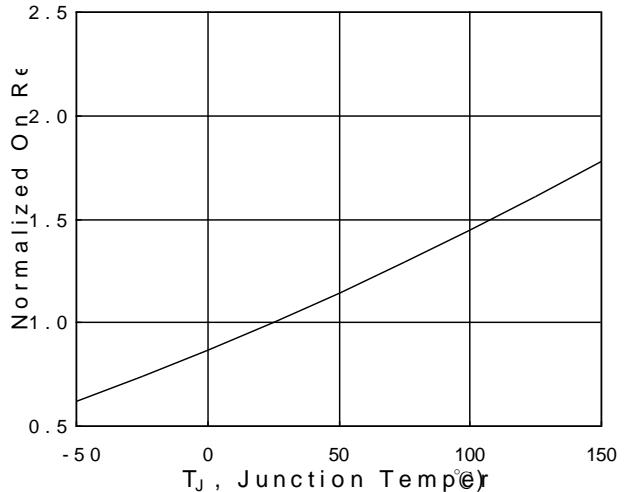
➤ N-Channel Typical Characteristics

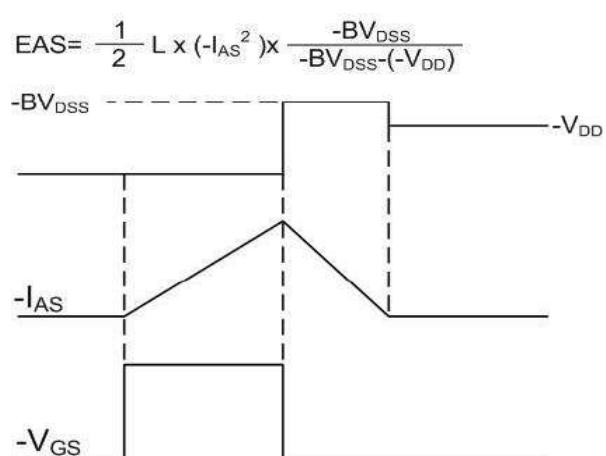
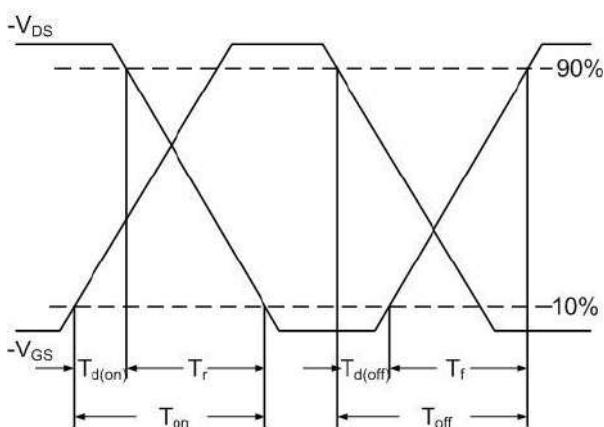
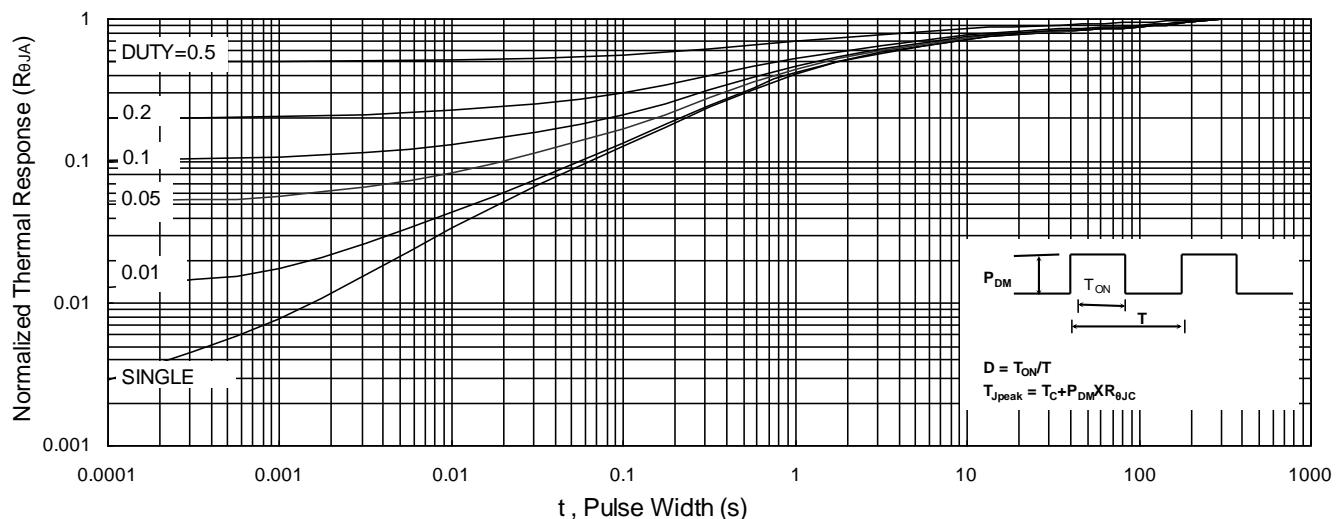
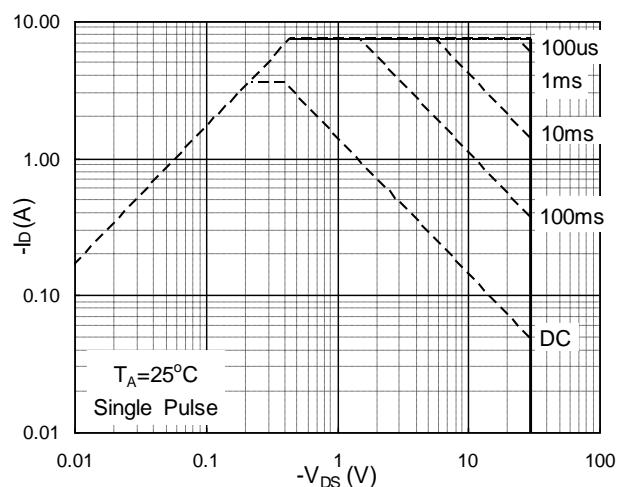
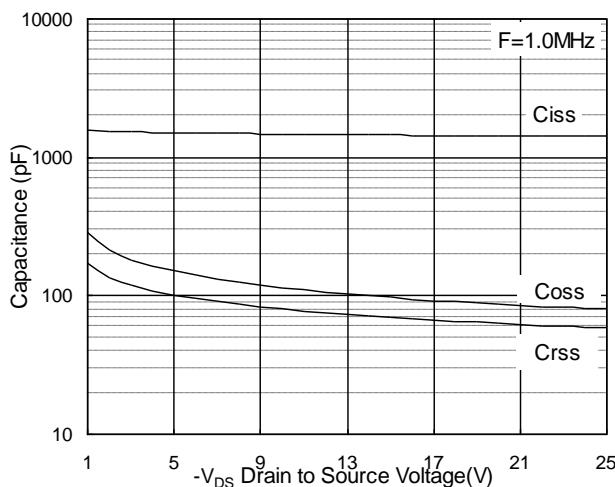

Fig.1 Typical Output Characteristics

Fig.2 On-Resistance v.s Gate-Source

Fig.3 Forward Characteristics of Reverse

Fig.4 Gate-Charge Characteristics

Fig.5 Normalized $V_{GS(th)}$ v.s T_J

Fig.6 Normalized $R_{DS(on)}$ v.s T_J

N-Ch and P-Ch Fast Switching MOSFET
V_{DS}=60V, I_D=4.8A, R_{DS(ON)}=32mΩ
V_{DS}=-60V, I_D=-3.7A, R_{DS(ON)}=70mΩ

Fig.7 Capacitance

Fig.8 Safe Operating Area

Fig.9 Normalized Maximum Transient Thermal Impedance

Fig.10 Switching Time Waveform

Fig.11 Unclamped Inductive Waveform

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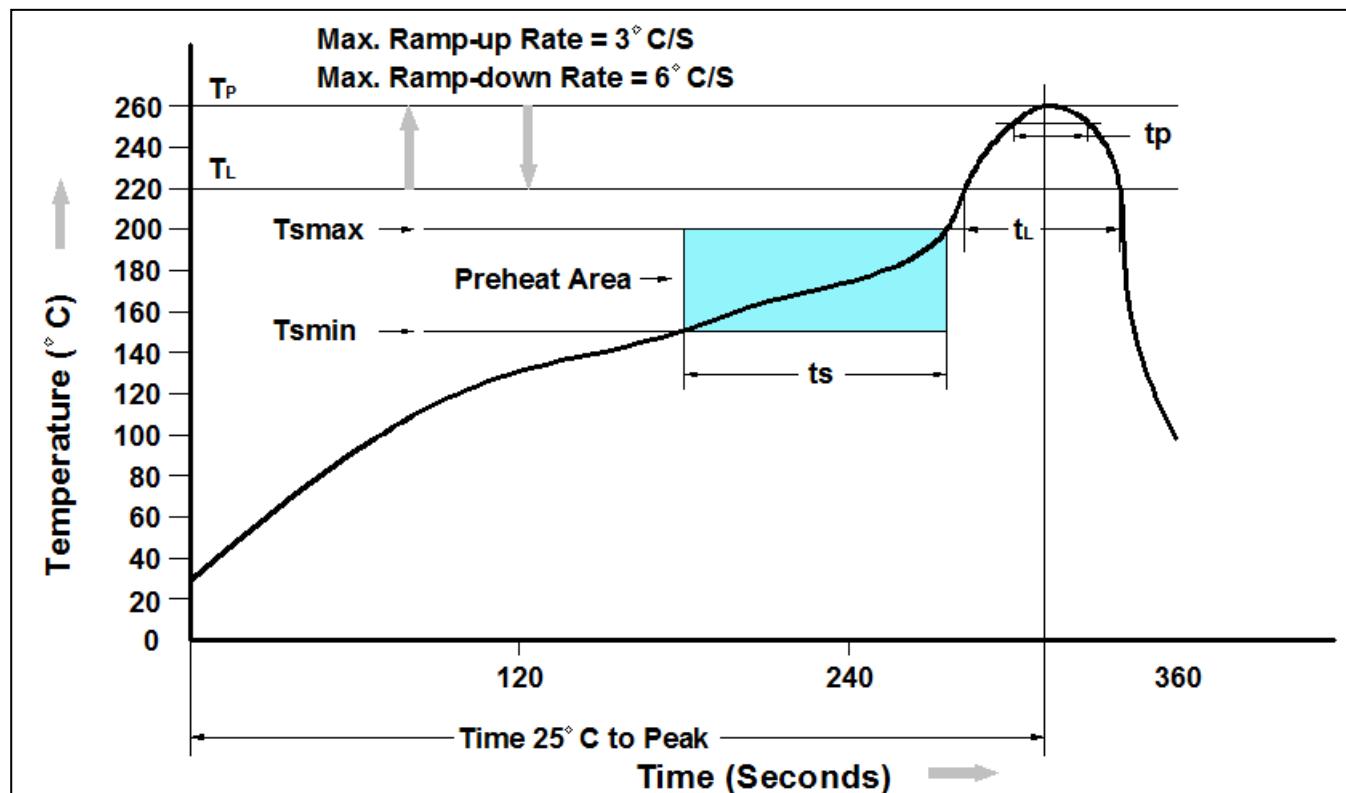
➤ P-Channel Typical Characteristics


Fig.1 Typical Output Characteristics

Fig.2 On-Resistance v.s Gate-Source

Fig.3 Forward Characteristics of Reverse

Fig.4 Gate-Charge Characteristics

Fig.5 Normalized $V_{GS(th)}$ v.s T_J

Fig.6 Normalized $R_{DS(on)}$ v.s T_J

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V_{DS}=60V, I_D=4.8A, R_{DS(ON)}=32mΩ
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➤ Recommand IR Reflow Soldering Thermal Profile



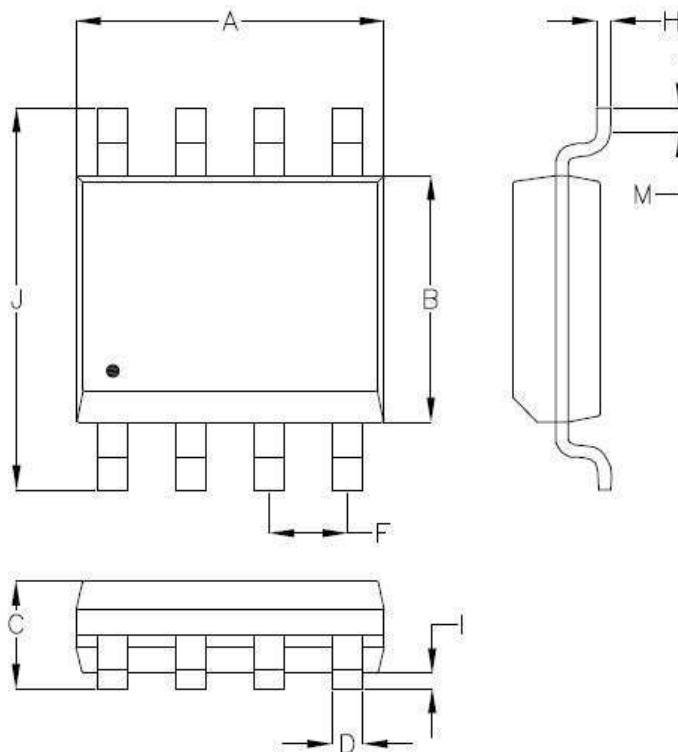
Profile Feature	Pb-Free Assembly Profile
Temperature Min. (Tsmin)	150°C
Temperature Max. (Tsmax)	200°C
Time (ts) from (Tsmin to Tsmax)	60-120 seconds
Average Ramp-up Rate (tL to tP)	3°C/second max.
Liquidous Temperature (TL)	217°C
Time (tL) Maintained Above (TL)	60 – 150 seconds
Peak Temperature	260°C +0°C / -5°C
Time (tp) within 5°C of actual Peak Temperature	30 seconds
Ramp-down Rate (TP to TL)	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.

➤ Ordering Information

Part Number	Description	Quantity
PAC69TJ01J	SOP-8 Reel	2500 pcs

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➤ **Package Information (SOP-8)**



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.700	5.150	0.185	0.203
B	3.700	4.100	0.146	0.161
C	1.23	1.753	0.048	0.069
D	0.310	0.510	0.012	0.020
F	1.070	1.470	0.042	0.058
H	0.160	0.254	0.006	0.010
I	0.050	0.254	0.002	0.010
J	5.750	6.250	0.226	0.246
M	0.400	1.270	0.016	0.050

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